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Coordinators

Dr. B. Khaleelu Rehman

Assoc. Prof., ECE Dept.

Dr. P. Ramesh Babu

Assoc. Prof., IT Dept.

Co-coordinators

Dr. P. Anuradha

Assoc. Prof., ECE Dept

Smt. E. Ramalakshmi
Asst. Prof., IT Dept.

Eligibility criteria to Register

- B.Tech & M.Tech (undergoing or completed) of ECE, CSE/IT, EEE and all allied branches
- Faculty members
- Working professionals in chip design
- Registration is free

Contact:

T. Sanjana : 8688185007

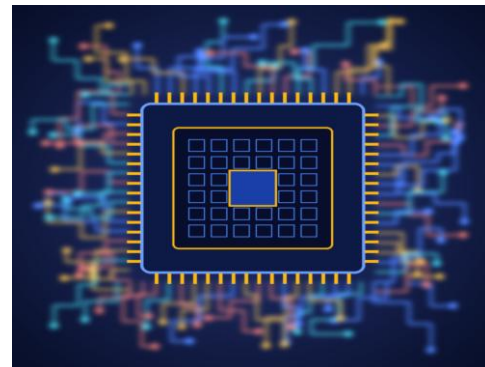
G. Jeetha sri :9490686459

A Five-day Boot camp on

“Semiconductor Design: RTL, IP Integration and SoC Signoff”

Using Open-Source EDA Tools

October 07 - 13, 2026



Organized by
Department of Electronics and
Communication Engineering &
Department of Information Technology

In Association with



National Institute of
Electronics &
Information
Technology (NIELIT),
Hyderabad



Ministry of
Electronics and
Information
Technology,
Government of India

ABOUT THE INSTITUTE

Chaitanya Bharathi Institute of Technology (CBIT), established in 1979, is a premier engineering institute in Telangana. It is UGC Autonomous, accredited with A++ Grade by NAAC, and its programmes are accredited by NBA. Spread across 50.8 acres, CBIT offers 12 UG and 10 PG programmes with a strong focus on education, research, innovation and industry collaboration. It was placed in the 151–200 band in NIRF Engineering 2025

ABOUT ECE DEPARTMENT

The Department of Electronics and Communication Engineering (ECE) at CBIT focuses on electronics, communication, embedded systems and VLSI technologies. It offers UG and PG programmes supported by well-equipped laboratories and research facilities. The department emphasizes technical skills, research, innovation, internships and industry interaction, preparing students for careers in core electronics and emerging technologies.

ABOUT IT DEPARTMENT

The Department of Information Technology (IT) at CBIT focuses on software development, information technologies and emerging computing areas. It offers B.E. in Information Technology with modern laboratories and industry-oriented learning. The department emphasizes technical skills, research, internships, placements and innovation to prepare students for the IT industry.

ABOUT NIELIT

NIELIT Hyderabad, under MeitY, Government of India, provides training and skill development in electronics, IT and emerging technologies, supporting students and professionals through industry-oriented programmes.

COURSE FEE

NIL – 100% FREE (no fee)

Just learning. Limited seats!

OBJECTIVES

- Learn industry-relevant semiconductor design skills
- Hands-on practice on RTL design and coding
- Understand functional verification
- Synthesis using open-source tools
- IP integration and SoC signoff
- Guidance from experts and industry practitioners

TOPICS

- RTL Design & Coding (System Verilog)
- Functional Verification
- Synthesis using Open Source Tools
- IP Integration
- SoC Signoff (DFT, STA, Lint)
- From Concept to Silicon – Your Design Journey

RESOURCE PERSONS

All sessions are conducted by experienced experts and faculty in Semiconductor Technology. Participants receive certificates upon successful completion and assessment. A live assessment will be conducted to evaluate the knowledge gained during the boot camp.

Scan for Registration



Registration link

<https://forms.gle/68nkPSuWhvX9AG8A7>