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Chaitanya Bharathi Institute of Technology
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NOTIFICATION

Ref. No: ¹⁹Projects/HR/CBIT/Advt./2025

Date: 18th October 2025

The Chaitanya Bharathi Institute of Technology (CBIT), Gandipet, Hyderabad – 500075 invites applications for the post of Project Associate-II/I (PA-II/I) under the ongoing MeitY-C2S project. The appointment is initially for a period of six months and is likely to be extended for the duration of the project depending on the performance. The appointment is purely temporary and does not confer any right whatsoever to claim for absorption into the Institution. The Institute shall not have any liability in respect of the continuation of service or otherwise. An undertaking to this effect shall have to be submitted at the time of joining the duty.

Project Title: The Design, Fabrication and development of Silicon Proven IP Core for High Resolution ADPLL (5 Years)				
Post	Essential Qualifications	Desirable Qualification	Pay-per month	Age Limit
Project Associate – I (PA-I)	1) Post Graduate / Graduate degree in Electronics and Communication Engineering or equivalent with first class preferably in VLSI or related fields	1. GATE/NET qualified 2. Experience in Analog IC Design and Cadence Virtuoso. 3. Experience in using MATLAB Simulink. 4. Experience in Verilog HDL	Rs. 31,000+ HRA (for GATE/NET qualified) Or Rs.25,000+ HRA (for others)	35 yrs (Age relaxation as per DST norms)
Project Associate – II (PA-II)	1) Post Graduate / Graduate degree in Electronics and Communication Engineering or equivalent with first class preferably in VLSI or related fields 2) Minimum 2-years experience in Research and Development	1. GATE/NET qualified 2. Experience in Analog IC Design and Cadence Virtuoso. 3. Experience in using MATLAB Simulink. 4. Experience in Verilog HDL	Rs. 35,000+ HRA (for GATE/NET qualified) Or Rs.28,000+ HRA (for others)	35 yrs (Age relaxation as per DST norms)

Duly filled application in the attached format enclosed in a cover with superscription “**Application for the post of PA-I in ECE Department**” hard copy should reach the undersigned on or before 4:00 P.M of **25 Oct 2025** and same may be mailed to jahangir_ece@cbit.ac.in with a copy to principal@cbit.ac.in and hr@cbit.ac.in and also interviews will be held on 30 Oct 2025.. Self attested Xerox copies of educational qualifications and experience in support of your claim should be enclosed along with the application. The prescribed qualifications are minimum and the mere fact that a candidate possessing the same will not entitle him/her for being called for written test and/ interview. The Institution reserves the right to restrict the candidates to be called for interview to a reasonable number on the basis of qualifications and experience higher than the minimum prescribed. The Institute also reserves the right either to fill up or not to fill up the posts. For any query the candidates may contact: **Dr.Mohd Ziauddin Jahangir**, Principal Investigator (cell: +91-8074885588).


Principal