



Chaitanya Bharathi Institute of Technology (A)

Department of Electronics and Communication Engineering

1.3.4: Students undertaking Field Projects/Internships

M.E (ECE-ESVLSID)

AY: 2022-23

INDEX

Name of the Student	Roll Number	Page Number
PULLA NAGENDRA VARA PRASAD	160121744401	2
GUDURI PAVANI	160121744402	3
NAAMA KAVYA SREE	160121744403	4
PANDALA CHARISHMA	160121744404	5
P SHIRISHA	160121744405	6
DAYYALA BALRAM	160121744406	7
DEVOLLA MANICHANDANA	160121744409	8
YENNATI NAGAVENI	160121744410	9
MOVVA SAILAXMI	160121744411	10

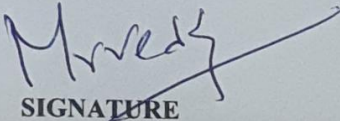
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CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)
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BONAFIDE CERTIFICATE

This is to certify that the dissertation work titled "Design and Verification of AHB-I2C Interface using System Verilog" Submitted by Pulla Nagendra Vara Prasad bearing Roll No. 1601-21-744-401, a student of Department of ECE, Chaitanya Bharathi Institute of Technology, in Partial fulfillment of the requirements for award of the degree of Master of Engineering with Embedded Systems and VLSI Design as specialization is a record of the bonafide work carried out by her during the academic year 2022-2023.



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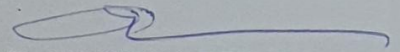
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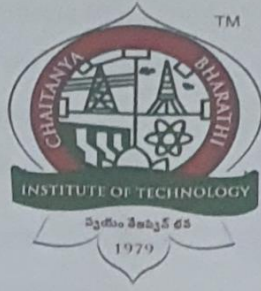
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This is to certify that the dissertation work titled “**DESIGN OF LOW NOISE AMPLIFIER USING ADS** ” submitted by **Guduri Pavani** bearing Roll No. **1601-21-744-402**, a student of **Department of ECE, Chaitanya Bharathi Institute of Technology**, in partial fulfillment of the requirements for award of the degree of **Master of Engineering with Embedded systems and VLSI Design** as specialization is a record of the bonafide work carried out by her during the academic year 2022-2023.

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Certificate

This is to certify that the major project work titled “**Design and Analysis of Vedic Multiplier using Carry Look Ahead Adder and Koggestone Adder**” submitted by N.Kavyasree bearing Roll No.1601-21-744-403, a student of Department of ECE, Chaitanya Bharathi Institute of Technology, in partial fulfillment of the requirements for award of the degree of **Master of Engineering with Embedded systems and VLSI Design** as specialization is a record of the Bonafide work carried out by her during the academic year 2022-2023.

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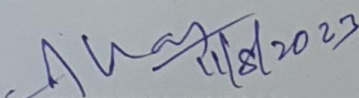


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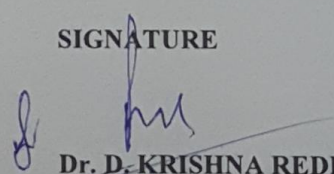
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This is to certify that the dissertation work titled “**HIGH SPEED RE-SETTABLE DELAY LINE TDC IN 90nm**” submitted by **Pandala Charishma** bearing **Roll No. 1601-21-744-404**, a student of **Department of ECE, Chaitanya Bharathi Institute of Technology**, in partial fulfillment of the requirements for award of the degree of **Master of Engineering with Embedded systems and VLSI Design** as specialization is a record of the bonafide work carried out by her during the academic year 2022-2023.

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This is to certify that the dissertation work titled “**High Frequency Low Phase Noise Voltage Controlled Ring Oscillator**” Submitted by **Pendyala Shirisha** bearing Roll No. **1601-21-744-405**, a student of Department of ECE, Chaitanya Bharathi Institute of Technology, in Partial fulfillment of the requirements for award of the degree of Master of Engineering with Embedded Systems and VLSI Design as specialization is a record of the bonafide work carried out by her during the academic year 2022-2023.

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This is to certify that the major project work titled “**Coverage Driven Verification of Serial Peripheral Interface(SPI) Shift Register In System Verilog Testbench Architecture**” submitted by **D.Balram** bearing **Roll No.1601-21-744-406**, a student of **Department of ECE, Chaitanya Bharathi Institute of Technology**, in partial fulfillment of the requirements for award of the degree of **Master of Engineering with Embedded systems and VLSI Design** as specialization is a record of the Bonafide work carried out by his during the academic year 2022-2023.

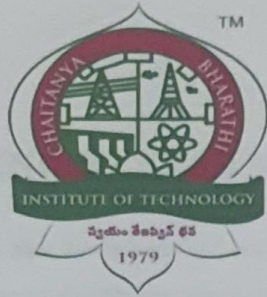
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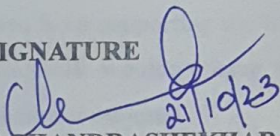


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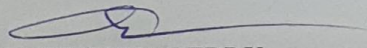
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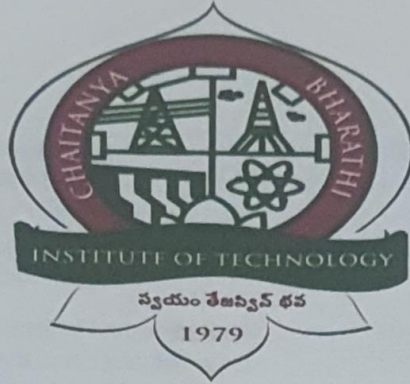
This is to certify that the dissertation work titled “A TIQ BASED 6 BIT TWO CHANNEL TIME INTERLEAVED ADC DESIGN USING 90NM TECHNOLOGY ” submitted by DEVOLLA MANICHANDANA bearing Roll No. 1601-21-744-409, a student of Department Of ECE, Chaitanya Bharathi Institute Of Technology, in partial fulfillment of the requirements for award of the degree of Master Of Engineering with Embedded Systems And VLSI Design as specialization is a record of the bonafide work carried out by her during the academic year 2022-2023.

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Certified that the phase-I dissertation work title “**DESIGN AND ANALYSIS OF AMBA APB BRIDGE PROTOCOL USING TCAMS IN VERILOG**” submitted by **Yennati Nagaveni** Bearing Roll No. 1601-21-744-410, a student of **Department of ECE, Chaitanya Bharathi Institute of Technology, Hyderabad**, in partial fulfilment of the requirements for award of the degree of **Master of Engineering with Embedded systems and VLSI Design** as specialization is a record of the bonafide work carried out by her during the academic year 2022-23.

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This is to certify that the major project work titled “**HYBRID ELECTRIC VEHICLE USING MATLAB SIMULINK**” submitted by **MOVVA SAI LAXMI** bearing **Roll No.1601-21-744-411**, a student of **Department of ECE, Chaitanya Bharathi Institute of Technology**, in partial fulfillment of the requirements for award of the degree of **Master of Engineering with Embedded systems and VLSI Design** as specialization is a record of the bonafide work carried out by her during the academic year 2022-2023.

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