

## **AY 21-22**

### **International Journals**

1. Kamlikar Sowmya – 160119744402, **Alenoor Krishna Kumar**, Design and Simulation of Area Efficient Carry Select Adder using Verilog HDL, Design Engineering (Toronto), vol no: 2021, Issue no: 7, pp: 7206 - 7216, August - 2021, ISSN: 0011-9342 (Scopus Indexed)

### **National Conference**

1. Amgoth Hariprasad, **Supraja Reddy Ammana** “Generation and Evaluation of IRNSS Navigation data for Spoofing Applications”, Research Day, CBIT, 28 August 2021.
2. Asra Fathima, **D.Nagadevi**, "Implementation of Deep Learning Architectures for Classification of Skin Lesions", Research Day, CBIT, 28 August 2021.
3. Nomula Rajitha, **A.Satyavati**, "Implementation of Deep Neural Networks for Multi-Classification of Brain Tumor Images", Research Day, CBIT, 28 August 2021.
4. Kukatla Ramprasad, **G.V. Pradeep Kumar**, "Implementation of Impatient Backoff Algorithm for wireless Adhoc networks", Research Day, CBIT, 28 August 2021.
5. **N. Alivelu Manga**, S. Sai Sandeep Kumar, "Design And Simulation of Approximate Multiplier for Low Power Applications", Research Day, CBIT, 28 August 2021.