

Chaitanya Bharathi Institute of Technology (Autonomous)

Department of Electronics and Communication Engineering Program: M.E. (Embedded Systems & VLSI Design)

1. PO Assessment – Batch 2019-2021

A. Direct PO Attainment:

S.No	Course	PO-1	PO-2	PO-3	PO-4	PO-5
1	Analog and Digital CMOS VLSI Design	1.712	1.775	1.39	1.6525	1.6525
2	Microcontrollers and Programmable Digital Signal Processors	1.624	1.6	1.6057	1.52	1.651
3	Research Methodology and IPR	1.896	1.896	1.896	1.896	1.896
4	Advanced computer organization	1.426	1.386	1.416	1.29	1.436
5	VLSI Technology and Physical Design Automation	1.772	1.11	1.9375	-	-
6	English for Research Paper Writing	-	-	2.28	2.28	2.28
7	Analog and Digital CMOS VLSI Design Lab	1	1	-	1	-
8	Microcontrollers and Programmable Digital Signal Processors Lab	1.4	1.4	1.4	1.4	1.4
9	Embedded System Design using RTOS	2.088	2.18	2.04	2.712	-
10	VLSI Design Verification and Testing	1.754	1.514	1.646	1.74	1.716
11	Low Power VLSI Design	1.656	1.7925	1.565	1.5	-
12	SoC Design	0.746	0.739	0.73	0.7975	0.7975
13	Value Education	-	-	2.28	2.28	2.28
14	RTL Simulation and Synthesis with PLDs Lab	3	3	-	3	-
15	RTOS and VLSI Design Verification Lab	3	3	-	3	-
16	Mini Project with Seminar	3	-	3	3	3
17	CPLD&FPGA (Program Elective)	2.322	2.302	2.177	2.291	2.274
18	Cost Management of Engineering Projects(open elective)	-	-	2.088	2.088	2.088
19	Dissertation /Phase-I	3	3	3	3	3
20	Dissertation /Phase-II	2.333333333	2.2	2.25	2.2	2.2
	PO-Attainment	1.98	1.87	1.92	2.03	1.98

B. Indirect PO Attainment:

	PO1	PO2	PO3	PO4	PO5
PO_INDIRECT	3	3	3	3	3

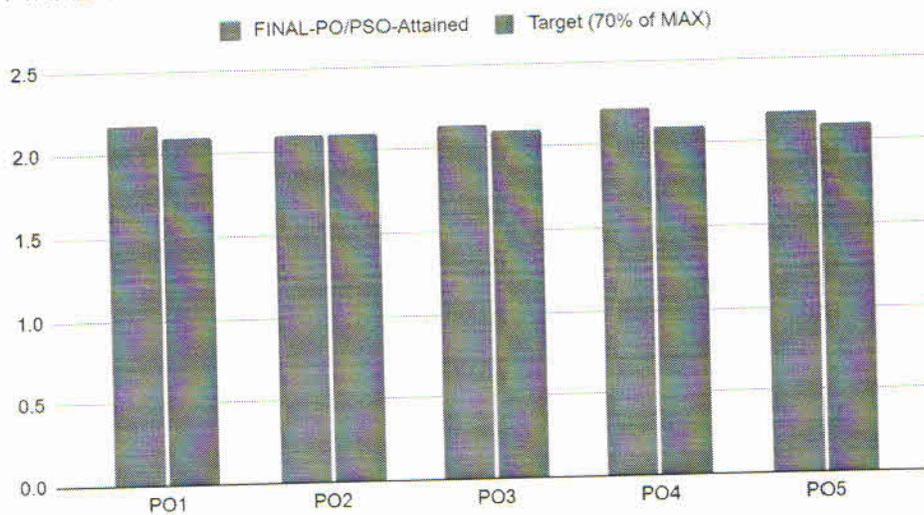
C. Final PO Attainment

	PO1	PO2	PO3	PO4	PO5
PO-DIRECT-Attainment	1.98	1.87	1.92	2.03	1.98
PO-INDIRECT-Attainment	3	3	3	3	3
FINAL-PO/PSO-Attainment	2.18	2.1	2.14	2.22	2.18

D. PO Analysis

	PO1	PO2	PO3	PO4	PO5
PO Maximum Achievable	3	3	3	3	3
Target (76% of MAX)	2.1	2.1	2.1	2.1	2.1
FINAL-PO/PSO-Attainment	2.18	2.1	2.14	2.22	2.18
Result	Attained	Attained	Attained	Attained	Attained

FINAL-PO/PSO-Attained and Target (70% of MAX)



Individual Proof of all Courses and for any other details, visit the CBIT-ECE Dept.'s OBE-website:

https://sites.google.com/a/cbit.ac.in/cbit_ece_obe/home

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2. PO&PSO Attainment Levels and Actions for Improvement- (2020-21)

-All the POs were Attained.



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