CHAITANYA BHARATI INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
Stake holder involvement in Curriculum Development
AY 2019-20
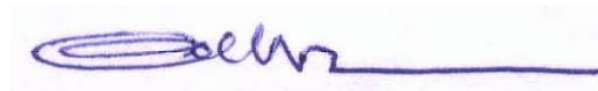**Suggestions Received From Stake Holders**

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Chaitanya Bharathi Institute of Technolog
Hyderabad-500 075

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
Stake holder involvement in Curriculum Development
AY 2019-20
Suggestions Received From Stake Holders

1) Industry

S.no	Name of the Industry expert	Company & Designation	Mobile No Mail ID	Suggestions & opinion
1	Aashrith	Mendel AI, USA	+1(979) 422-0432 taashrith@gmail.com	In VII Semester there is need to understand the theoretical concepts of computer networks, and laboratory may be introduced for the same.
2	U Konda Reddy	CHIPEDGE	8977444150 ukreddy@gmail.com	Less emphasis on Random Process for the subject Analog Communication

1) Industry (Proof)

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2

Date : 10-04-2021

Dear Sir,

Sub : Feedback on curriculum followed in ECE DEPT. OF CBIT

1. To improve and understand the theoretical concepts of computer networks, laboratory may be introduced.

2. Apart from INTEL microcontroller architecture, Advanced microcontroller architecture of ARM can be included in the subject microcontroller.

Signature:

Name : Aashrith

Organization : Mendel AI, USA
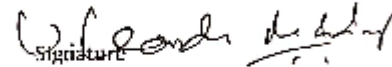
Mail id : taashrith@gmail.com

Mobile no : +1(979) 422-0432

Date : 15-07-2020

Dear Sir,

Sub : Feedback on curriculum followed in ECE DEPT. OF CBIT

1. Less emphasis on Random Process for the subject Analog Communication

2. Apart from INTEL Microcontroller Architecture, Advanced Microcontroller Architecture
   of ARM can be included in the subject Microcontroller.
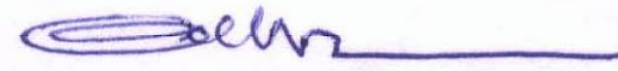
Signature

Name : KONDA REDDY. U

Organization : Chip edge Technologies

Mail id : ukreddy1@gmail.c

Mobile no : 8977444150

2). Alumni

S.No	Name of the Alumni	E mail Id & Ph#	Batch	Present occupation	Suggestions
1	K Prasad	kprasad@gmail.com	2016-20		As per the need of the day a separate course on Embedded systems and Software Defined Radio must be introduced.
2	Ch Akhila	akhilach@gmail.com	2016-20		Less emphasis was given on Random processes in Analog Communication Course
3	B. Priyanka	priyanka@gmail.com	2015-19		A separate course on Telecommunication can be introduced in the syllabus which helps to build the career in this field.
4	P Amitha Shreshta	amithashreshta@gmail.com	2015-19		Enhance the Microcontroller Lab with some advanced and latest experiments as it forms the basis for Embedded Systems and IoT

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2). Alumni(proof)

10-04-2021

Dear Madam,

The curriculum designed for students is very much appropriate but some changes may be incorporated if possible. With the growing technology and as per the need of the day a separate course may be introduced on Embedded Systems and Software Defined radio.

Thank You

regards

Prasad

K. Prasad

Roll No: 161

Batch: 2016 - 2020

Dear Madam,

The curriculum designed for students is very much appropriate and very good but in Analog

Communication I have noticed that less emphasis was given on Random Process.

If possible make some changes.

Thank You

Regards

CH·Akhila

Roll No: 160116735 121

Batch - 2016 to 2020

15-07-2020

Dear Madam,

The curriculum designed for students is very much appropriate and very good but I feel that a separate course telecommunications may be introduced which may help the students to build their career in this field also.

Thank You

Regards

Priyanka. B

(160915735073)

(2015-19)

15-07-2020

Dear Madam,

In the current scenario Microcontrollers form the basis for Embedded systems and IoT. My sincere advice is to enhance the Microcontroller lab with some advanced and latest experiments
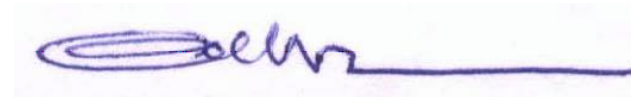
Thank You

Regards

Anitha .P

(16 0 115 735 001)

(2015 – 19)

3). Parent

S.no	Name Of the Parent	Name of the student	Batch	Mobile No Mail ID	Present parent occupation	Suggestions &opinion
1.	Dr K Sudershan Reddy	K Suraj Reddy	2010-14	9989793 931	MD, VIRUPAKSHA Pharama	After thorough reading of curriculum structure and contents in each course, my suggestion is to introduce Verilog and VLSI related courses in the curriculum as basics of these are already covered in IV semester DSD subject.

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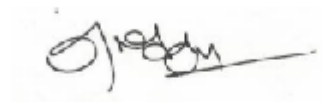Chaitanya Bharathi Institute of Technolog
Hyderabad-500 075

3). Parent (Proof)

Date: 15.07.2020

Respected Head of the department,

After thorough reading of curriculum structure and contents in each course, my suggestion is to introduce Verilog and VLSI related courses in the curriculum as basics of these are already covered in IV semester DSD subject.
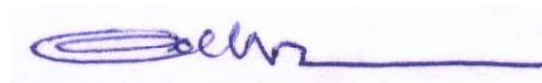
Thanking you sir ,

Signature of the parent

4) Faculty

S.no	Name of the faculty	Designation	Mobile No Mail ID	Suggestions
1	Sri Md Ziauddin Jhangir	Assistant Professor	Jahangir_ece@cbit.ac.in 9885717845	In RTOS, if possible case studies may be added for better understanding.
2	Sri MV Nagabhushanam	Assistant Professor	nagabhushanam_ece@cbit.ac.in 9059990885	The title of the course may be changed from Electronic Instrumentation to Electronic Measurements and Instrumentation.
3	Sri MV Nagabhushanam	Assistant Professor	nagabhushanam_ece@cbit.ac.in 9059990885	In EMI course include the topics on AC and DC bridges.
4	Sri MV Nagabhushanam	Assistant Professor	nagabhushanam_ece@cbit.ac.in 9059990885	A topic on virtual Instrumentation Lab View may be included

4) Faculty (proof)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FROM FACULTY ON CURRICULUM DEVELOPMENT

Date : 5/7/2020

Name of the faculty : MOHD ZIAUDDIN JAHANGIR

Designation ASSISTANT PROFESSOR

Course name REAL TIME OPERATING SYSTEM

Suggestions

1. Need to add case study for better understanding of course

2. Sample programs need to be included for demonstration

Signature of the faculty

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FROM FACULTY ON CURRICULUM DEVELOPMENT

Date : 5/7/2020

Name of the faculty : M.V. NAGABHUSHANAM
Designation : Assistant Professor
Course name : ELECTRONIC INSTRUMENTATION (16EC C12)

Suggestions : to

1. change the course title as "Electronic Measurements and Instrumentation" in R-18 curriculum
2. include the topis on DC bridges
3. include the topis on AC bridges.
4. include the topic on virtual instrumentation (Labview)

Signature of the faculty