

CHAITANYA BHARATI INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Stake holder involvement in Curriculum Development

AY 2018-19

Suggestions Received From Stake Holders

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HEAD
DEPARTMENT OF ECE
Chaitanya Bharathi Institute of Technolog
Hyderabad-500 075

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
Stake holder involvement in Curriculum Development
AY 2018-19
Suggestions Received From Stake Holders

1) Industry

S.no	Name of the Industry expert	Company & Designation	Mobile No Mail ID	Suggestions & opinion
1	Aashrith	Mendel AI, USA	+1(979) 422-0432 taashrith@gmail.com	Apart from INTEL Microcontroller Architecture, Advanced Microcontroller architecture of ARM can be included in the subject microcontroller.
2	U Konda Reddy	ARICENT Holdings Ltd.	8977444150 ukreddy@gmail.com	Less focus on Exponential Fourier Series in the subject "Signal and Systems" is noticed.
3	U Konda Reddy	ARICENT Holdings Ltd.	8977444150 ukreddy@gmail.com	Less Weightage for applications of multirate DSP in the subject digital signal processing is found.



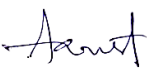
1) Industry(Proof)

HEAD
DEPARTMENT OF ECE
Chaitanya Bharathi Institute of Technolog
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Dear Sir,

Sub : Feedback on curriculum followed in ECE DEPT. OF CBIT

1. To improve and understand the theoretical concepts of computer networks, laboratory may be introduced.
2. Apart from INTEL microcontroller architecture, Advanced microcontroller architecture of ARM can be included in the subject microcontroller.

Signature: 

Name : Aashrith

Organization : Mendel AI, USA

Mail id : taashrith@gmail.com


Mobile no : +1(979) 422-0432

Date : 15-03-2019

Dear Sir,

Sub : Feedback on curriculum followed in ECE DEPT. OF CBIT

1. Less focus on Exponential Fourier Series in the subject "Signal and Systems" is noticed.
2. Less Weightage for applications of multirate DSP in the subject digital signal processing is found.


Signature
Name : KONDA REDDY. U
Organization : Aricent
holdings Ltd
Mail id : ukreddy1@gmail.
Mobile no : 8777444150

2). Alumni

S.No	Name of the Alumni	E mail Id & Ph#	Batch	Present occupation	Suggestions
1	P Vikas	vikasp@gmail.com	2013-17		In Computer Architecture and Microprocessors I have noticed that to Analyze the Computer architecture concepts, an application is needed to be incorporated as any architecture of the processor.
2	K Likitha	Likithaa.maradani@gmail.com 9618264011	2016-20		In Coding Theory and Techniques, importance of decoding algorithm is missing. Priority must be given to the codes based on abstract algebra.



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2). Alumni (Proof)

Dear Madam,

The curriculum designed for students is very much appropriate and very good but I feel that in Computer Architecture and Microprocessors I have noticed that to Analyse the Computer architecture concepts, an application is needed to be incorporated as any architecture of the processor.

If possible make some changes.

Thank You

Regards

Vikas .P.

(P. VIKAS)

(16011373512)

(2013-17)

15-03-2019

Dear Madam,

The curriculum designed for students is very much appropriate and very good but I feel that in Coding Theory and Techniques I have noticed

1. The importance of decoding algorithms is missing
2. Priority must be given to the codes based on abstract algebra

If possible make some changes.

Thank You

Regards

Likitha. K)

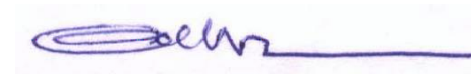
K. LIKITHA

(160113735068)

(2013-17)

3). Parent

S.no	Name Of the Parent	Name of the student	Batch	Mobile No Mail ID	Present parent occupation	Suggestions &opinion
1.	HEPSIBA H JOHN MURRAY	Michael Joseph Murray	2017- 21	9492922 808		It is suggested to introduce a course on current trending topics such as Block chain technology, cryptography and Cyber security so that the students will have good exposure to the industry needs.



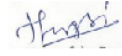
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3). Parent (Proof)

Date: 15-03-2019

Respected Head of the department,

It is suggested to introduce a course on current trending topics such as Block chain technology, cryptography and Cyber security so that the students will have good exposure to the industry needs.

A handwritten signature in blue ink, appearing to read 'Hepsibah', written over a light blue rectangular background.

Signature of the Parent
(HEPSIBAH JOHN MURRAY)

4) Faculty

S.no	Name of the faculty	Designation	Mobile No Mail ID	Suggestions
1	Dr Ramana Reddy	Assistant Professor	mramanareddy_ece@cbit.ac.in 9849634083	In Network Theory, include topics on network analysis and network synthesis. Also include topics on symmetrical and asymmetrical network properties and analysis.
2	Dr A Supraja Reddy	Associate Professor	suprajareddy_ece@cbit.ac.in 9866064120	In Principles of GNSS, IRNSS architecture, signals, limitations and advantages may be included
3	Smt N Dhanalakshmi	Assistant Professor	dhanalakshmi_ece@cbit.ac.in 8919788767	In CAMP course 8086 microprocessor architecture and memory organization to analyze the computer architecture concepts may be included.



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4) Faculty(Proof)



CHAITANYABHARATHIINSTITUTE OF TECHNOLOGY(A)

Chaitanya Bharathi P.O., Kohapet (V), Gandipet (M), Hyderabad - 500 075, Telangana
State Phone No: 040-24193280; Fax No: 040-24193278; Website: www.cbti.ac.in

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FROM FACULTY ON CURRICULUM DEVELOPMENT

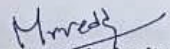
Date: 11/03/2019

Name of the faculty Dr.M.Ramana Reddy
Designation Assistant Professor
Course name Network Theory

Suggestions :

Please include

1. Topics on Network analysis and network synthesis.
2. Topics on Symmetrical and Asymmetrical network properties and analysis


Signature of the faculty



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Date: 15/03/2019

Name of the faculty Dr. A. Supriya Reddy
Designation Assoc. Prof
Course name Principles of GNS

Suggestions: The following Topics may be included
1) IRNS Architecture
signals
limitations
advantages

Please include

Signature of the faculty



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FROM FACULTY ON CURRICULUM DEVELOPMENT


Date : 12/4/2019

Name of the faculty Ms.N.Dhana Lakshmi
Designation Assistant professor
Course name CAMP

Suggestions :

Please include

1. Topics on 8086 Microprocessor architecture and memory organization to analyse the Computer architecture concepts.


Signature of the faculty