



Chaitanya Bharathi Institute of Technology (A)

(Affiliated to Osmania University)

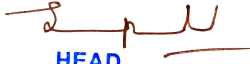
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

1.4.1 Structured feedback for design and review of syllabus

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A.Y: 2018-19

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Dept. of EEE, CBIT (A)
Gandipet, Hyderabad - 75



Chaitanya Bharathi Institute of Technology (A)

(Affiliated to Osmania University)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

1.4.1 Structured feedback for design and review of syllabus – semester- wise / year-wise is received from

- 1) Students,
- 2) Teachers,
- 3) Employers and
- 4) Alumni

Process of curriculum evaluation:

Curriculum of the program is designed by Course expert group(CEG) of the department, which is in line with Vision and Mission of the Institute/Departments, Program Outcomes, curricula of Reputed universities / Institutions, curriculum proposed by Statutory bodies and identified industrial needs.

The complete process is executed by four level system consists of :

- Course expert group (CEG)
- **Programme Assessment & Quality Improvement Committee(PAQIC)**
- Board of Studies(BoS)
- **Common Board of Studies**
- Academic Council (AC)
- Governing Body(GB)

Subject Expert Committee:

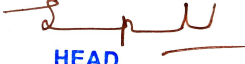
This committee comprises of Head of the Department, senior faculty members and Course Coordinators. Based on the assessment from the feedback of stakeholders, alumni and industry needs new course is framed or existing course is modified. Suggests the actions needed for further improvement. This committee will meet at least once in a semester.

Course expert group(CEG):

Board of Studies is constituted with Head of the Department (Chairman), professors of the department, one senior Associate Professor, Senior Assistant Professor, two experts from other prime universities / academic institutes, expert adviser from industry along with Alumni. The syllabus proposed (for both UG and PG program) by Course expert group(CEG) is reviewed in BoS meeting. BoS will meet at least once in a year.

Academic Council (AC):

The members of Academic Council are Principal, Deans, Professors and Heads of the departments, expert from reputed university and industry. Syllabus recommended by BoS is approved by Academic Council. This will meet at least once in a year.


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Governing Body:

Governing Body comprises of member from Board of Management, expert from reputed University, Industry experts, UGC nominee, Principal, two senior Professors. The academic regulations and curriculum designed and approved are accepted by Governing body.

Curriculum Design Process:

The process adopted to design the curriculum is as follows and shown in Fig-1.1:

- Course contents are prepared by a member of staff expert committee.
- Course objectives and outcomes are defined by expert members.
- The designed curriculum, collection of courses set by subject expert committee, is reviewed and approved in BoS and further in common BoS for all programmes.
- Curriculum approved by BoS is discussed in academic council and approved after review.
- The final curriculum is approved by the governing body of the institute.

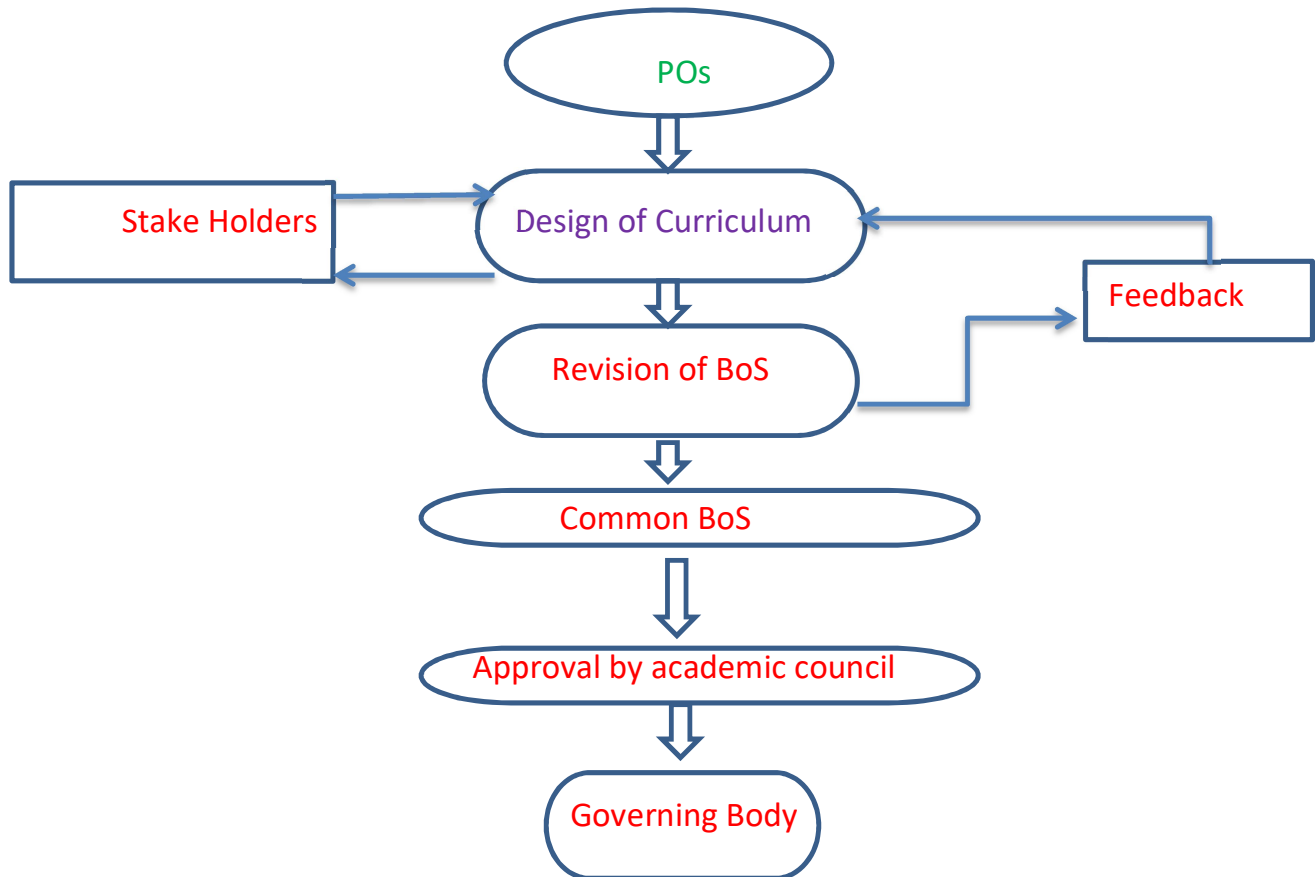


Fig: 1.1. Design of Curriculum

While designing the curriculum the following points are considered.

1. Vision and Mission of the Institute/Departments
2. Program outcomes
3. Curricula at IITs and NITs
4. Accreditation Criteria
5. Model curriculum proposed by AICTE
6. Industrial demand
7. Local Needs
8. Board of Studies (BoS) as per UGC norms: with senior faculty, subject experts, industry experts, alumni, etc.,
9. Stake holders Feedback.

In considering the above points the department has followed the following steps in framing and finalizing the curriculum and syllabus

- I. Department level discussions
- II. Common BoS meeting at the Institute level
- III. BoS meeting by individual departments
- IV. Common BoS meeting --- If required
- V. Academic Council Meeting
- VI. Governing Body Meeting
- VII. Finally, for Circulation and for Implementation.

Revision of syllabus has been done during academic year 2016-17 as per Choice Based Credit System (CBCS). Third revision was done during the academic year 2018-19 as per the AICTE Model Curriculum (MC).

In connection with the above mentioned process, Stake holder's feedback process plays an important role in Curriculum design.

The composition of the stakeholders of the programme- (Student, Alumni, Industry, Faculty, Employer and Parents as stake holders)

Student (30)

- Most prominent role in the program.
- Students feedback is considered to introduce innovative teaching and learning methodologies
- Students input will help in program to introduce the elective courses to meet current trends.

Faculty (14)

- Plays a vital role in executing of the program.
- Faculty involves in various committees to check the consistency and Quality of the program.
- Faculty provides inputs for designing the program, PEOs/POs establishment, Course Objectives and assessment.

Alumni (22)

- Focus group because they are a measure of the long-term success of our program.
- Alumni feedback helps in curriculum design to meet recent trends in engineering.
- Recollect their existence during their program study and advise the department with necessary inputs in point of student career.

Employer (5)

- Represents the major end users of our graduates.
- Gives higher focus to the program on future data to create awareness with current industry
- Gives input which bridges the gap between industry and Academics.

Parents

- Extends valuable advices to the department depending upon their background which compliments curriculum framework.
- Expects their wards in good professional career and higher education

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

Student Feedback- Academic Year:

Name of the Student : _____ **Department: EEE**

Name of the Department : _____


Name of the programme : _____

Academic Year : _____

<u>Excellent-5</u>	<u>Very Good-4</u>	<u>Good-3</u>	<u>Average-2</u>	<u>Poor-1</u>
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S.No	Parameters	Rating (5 being highest)				
1	The Syllabus Of the courses that you have studied, Relation to the competencies expected?					
2	The allocation of the credits to the courses?					
3	The offering of the Electives in terms of their relevance to specialization streams?					
4	The Electives offered in relation to the technological advancements?					
5	Rate the size of Syllabus in terms of the load on the students.					
6	The evaluation scheme designed for each of the course?					
7	The Objectives Stated for each of the course?					
8	The Percentage of course having LAB Components?					
Any other relevant information : 						

Signature of the Student


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**CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

Feedback from B.E Alumni - Academic Year:

Name:

Mobile No:

Email:

Year of Passing:


Alumni are the important stakeholders to our institution. Your feedback facilitates to correct and improve various existing processes in the institution. On behalf of the institute, we request you to spare your valuable time to give your feedback in the following format.

<u>Excellent-5</u>	<u>Very Good-4</u>	<u>Good-3</u>	<u>Average-2</u>	<u>Poor-1</u>
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S.No	Parameters	Rating (5 being highest)				
1.	How effectively is the knowledge acquired in the UG program at CBIT helping you in your career?					
2.	How effectively are you utilizing the acquired problem solving design/development skills in your professional life?					
3.	How useful is the project work/research-based approach you learnt in CBIT helping you in providing valid conclusions in your work?					
4.	How good are you at using modern engineering and Software tools in your work environment?					
5.	As a professional engineer, how actively are you working towards societal and environmental benefits?					
6.	How well has CBIT prepared you to be a life-long learner by following professional ethics/values?					
7.	How well do you think that your interaction with the faculty/guests/peers/juniors in CBIT helped you to communicate in your work environment?					
8.	To what extent has your involvement in the events organized in CBIT helped you to enhance your self-confidence, team work, leadership and managerial skills?					
9.	To what extent you are able to Analyze, synthesize, design and test Electrical & Electronic systems					
10.	Usefulness of our curriculum in the Industry					
11.	Usefulness of co-curricular/ extra-curricular activities at CBIT					

Any other relevant information :

Signature of Alumni


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CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)

Employer Feedback - Academic Year:

Name of the Company/Institute:

Name of the Evaluating Graduate with Designation:

Year:

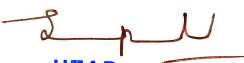
Employers are the important stakeholders to our institution. Your feedback facilitates to correct and improve various existing processes in the institution. On behalf of the institute, we request you to spare your valuable time to give your feedback in the following format.

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
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S.No	Parameters	Rating (5 being highest)				
1	Domain Knowledge and Aptitude Levels					
2	Problem analysis and design of appropriate solutions					
3	Attitude towards Research based approach					
4	Adaptability to new technology/tools and zeal to be a constant learner					
5	Commitment to work, managerial skills and ability to meet deadlines					
6	Work towards sustainable development, societal improvements and Environmental Benefits					
7	Professional ethics					
8	Communication Skills					
9	Team spirit, interpersonal relations and leadership skills					
10	How do you rate capability to analyze, synthesize, design, develop and test systems/processes?					
11	Overall Job performance					

Any other relevant information :

Signature of the Employer


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CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

FACULTY FEEDBACK ON CURRICULUM - Academic Year:

Name of the Faculty Member : _____ **Department: EEE**


Designation : _____

Courses Handled : _____

<u>Excellent-5</u>	<u>Very Good-4</u>	<u>Good-3</u>	<u>Average-2</u>	<u>Poor-1</u>
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S.No	Parameters	Rating (5 being highest)				
1	The design of the curriculum addresses the holistic development of student.					
2	The curriculum is well balanced with knowledge, skills and employability.					
3	<u>Suitability of the Syllabus to the Course.</u>					
4	The course/courses are relevant to the present scenario.					
5	<u>Course objectives and outcomes are well defined.</u>					
6	<u>Prescribed books/suggested readings and other references appropriate.</u>					
7	BoS members from Academia and Industry constructive in updating the syllabi according to the changing educational challenges and requirements in line with regulating bodies like AICTE, UGC etc.					
8	The scheme and evaluation schedules satisfy the Teaching Learning Process.					
9	Freedom to suggest/propose/modify/incorporate new topics in the syllabus during the revision of curriculum?					
10	Institute/Department gives the freedom to adopt new technologies/strategies of innovative teaching?					
11	The environment in the department is conducive to learning, teaching, and research.					
12	<u>Provisions/ for professional development.</u>					
13	Adequacy of infrastructure (class/staff rooms, labs, library, and ICT facilities) in the institute.					
Any other relevant information :						

Signature of the faculty


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CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

Student Feedback- Academic Year: 2018-19

Name of the Student : *Gunti Akash (160115734026)*
 Name of the Department : *EEE-D1*
 Name of the programme : *BE*
 Academic Year : *2018-19*

Department: EEE

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
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S.No	Parameters	Rating (5 being highest)				
1	The Syllabus Of the courses that you have studied, Relation to the competencies expected?	5				
2	The allocation of the credits to the courses?		4			
3	The offering of the Electives in terms of their relevance to specialization streams?	5				
4	The Electives offered in relation to the technological advancements?	5				
5	Rate the size of Syllabus in terms of the load on the students.	5				
6	The evaluation scheme designed for each of the course?		4			
7	The Objectives Stated for each of the course?	5				
8	The Percentage of course having LAB Components?		4			

Any other relevant information : -

Requested to change the Electrical circuits syllabus as per the GATE point of view.

Akash
Signature of the Student

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CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

Student Feedback- Academic Year: 2018-19

Name of the Student : Thandu Rahul (16011573404) Department: EEE
 Name of the Department : EEED1
 Name of the programme : BE
 Academic Year : 2018-19

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
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S.No	Parameters	Rating (5 being highest)				
1	The Syllabus Of the courses that you have studied, Relation to the competencies expected?	5	5			
2	The allocation of the credits to the courses?	5				
3	The offering of the Electives in terms of their relevance to specialization streams?	5				
4	The Electives offered in relation to the technological advancements?	5				
5	Rate the size of Syllabus in terms of the load on the students.	5				
6	The evaluation scheme designed for each of the course?	5				
7	The Objectives Stated for each of the course?	5				
8	The Percentage of course having LAB Components?	5				

Any other relevant information :

- ?

Rahul

Signature of the Student

[Signature]

Head
 Dept of EEE & BE
 Chaitanya Bharathi Institute of Technology

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

Student Feedback- Academic Year: 2018-19

Name of the Student : Sama Aravind (160115134084) Department: EEE
 Name of the Department : EEE-D2
 Name of the programme : B-E
 Academic Year : 2018-19.

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
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S.No	Parameters	Rating (5 being highest)				
1	The Syllabus Of the courses that you have studied, Relation to the competencies expected?	5				
2	The allocation of the credits to the courses?		3			
3	The offering of the Electives in terms of their relevance to specialization streams?	5				
4	The Electives offered in relation to the technological advancements?	5				
5	Rate the size of Syllabus in terms of the load on the students.	5				
6	The evaluation scheme designed for each of the course?		3			
7	The Objectives Stated for each of the course?	5				
8	The Percentage of course having LAB Components?		3			

Any other relevant information :

Include the Circuits Course as per gate syllabus to get better score for Gate Rank.

Aravind
 Signature of the Student



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 Gandipat, Hyderabad-75

**CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

Feedback from B.E Alumni - Academic Year: 2018-19

Name: B. Manish Kumar,
Mobile No: 9177911435
Email: Sainanish65@gmail.com,
Year of Passing: 2018

Alumni are the important stakeholders to our institution. Your feedback facilitates to correct and improve various existing processes in the institution. On behalf of the institute, we request you to spare your valuable time to give your feedback in the following format.

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
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S.No	Parameters	Rating (5 being highest)				
1.	How effectively is the knowledge acquired in the UG program at CBIT is helping you in your career?	5				
2.	How effectively are you utilizing the acquired problem solving design/development skills in your professional life?	5				
3.	How useful is the project work/research-based approach you learnt in CBIT helping you in providing valid conclusions in your work?	5				
4.	How good are you at using modern engineering and Software tools in your work environment?	5				
5.	As a professional engineer, how actively are you working towards societal and environmental benefits?		4			
6.	How well has CBIT prepared you to be a life-long learner by following professional ethics/values?	5				
7.	How well do you think that your interaction with the faculty/guests/peers/juniors in CBIT helped you to communicate in your work environment?	5				
8.	To what extent has your involvement in the events organized in CBIT helped you to enhance your self-confidence, team work, leadership and managerial skills?		4			
9.	To what extent you are able to Analyze, synthesize, design and test Electrical & Electronic systems	5				
10.	Usefulness of our curriculum in the Industry	5				
11.	Usefulness of co-curricular/ extra-curricular activities at CBIT	5				

Any other relevant information : In Ec-I & Ec-II some topics are not needed please Remove the topics and make it as one subject, instead of that a new course can be introduced.

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Signature of Alumni

**CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

Feedback from B.E Alumni - Academic Year: 2018-19

Name: D. Akhila
 Mobile No: 8790826029
 Email: akhilabharth007@gmail.com
 Year of Passing: 2018

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Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
-------------	-------------	--------	-----------	--------

S.No	Parameters	Rating (5 being highest)				
		5	4	3	2	1
1.	How effectively is the knowledge acquired in the UG program at CBIT is helping you in your career?	5				
2.	How effectively are you utilizing the acquired problem solving design/development skills in your professional life?	5				
3.	How useful is the project work/research-based approach you learnt in CBIT helping you in providing valid conclusions in your work?	5				
4.	How good are you at using modern engineering and Software tools in your work environment?	5				
5.	As a professional engineer, how actively are you working towards societal and environmental benefits?		4			
6.	How well has CBIT prepared you to be a life-long learner by following professional ethics/values?	5				
7.	How well do you think that your interaction with the faculty/guests/peers/juniors in CBIT helped you to communicate in your work environment?	5				
8.	To what extent has your involvement in the events organized in CBIT helped you to enhance your self-confidence, team work, leadership and managerial skills?			3		
9.	To what extent you are able to Analyze, synthesize, design and test Electrical & Electronic systems	5				
10.	Usefulness of our curriculum in the Industry	5				
11.	Usefulness of co-curricular/ extra-curricular activities at CBIT	5				
Any other relevant information :						
1. Gate syllabus Can be include Coae Subjects						
2. Some management Subjects Canbe Introduce.						

(Handwritten Signature)

(Handwritten Signature)
Signature of Alumni

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
Feedback from B.E Alumni - Academic Year: 2018-19

Name: G. Madhu

Mobile No: 9160916176

Email: Madhu.bbms@gmail.com

Year of Passing: 2018

Alumni are the important stakeholders to our institution. Your feedback facilitates to correct and improve various existing processes in the institution. On behalf of the institute, we request you to spare your valuable time to give your feedback in the following format.

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
-------------	-------------	--------	-----------	--------

S.No	Parameters	Rating (5 being highest)				
1.	How effectively is the knowledge acquired in the UG program at CBIT is helping you in your career?	5				
2.	How effectively are you utilizing the acquired problem solving design/development skills in your professional life?	5				
3.	How useful is the project work/research-based approach you learnt in CBIT helping you in providing valid conclusions in your work?	5				
4.	How good are you at using modern engineering and Software tools in your work environment?	5				
5.	As a professional engineer, how actively are you working towards societal and environmental benefits?	5				
6.	How well has CBIT prepared you to be a life-long learner by following professional ethics/values?	5				
7.	How well do you think that your interaction with the faculty/guests/peers/juniors in CBIT helped you to communicate in your work environment?	5				
8.	To what extent has your involvement in the events organized in CBIT helped you to enhance your self-confidence, team work, leadership and managerial skills?		4			
9.	To what extent you are able to Analyze, synthesize, design and test Electrical & Electronic systems	5				
10.	Usefulness of our curriculum in the Industry	5				
11.	Usefulness of co-curricular/ extra-curricular activities at CBIT	5				

Any other relevant information : -

1) Practical knowledge concepts have to introduce in core Subjects like Electrical machines and power Systems

2) Internships for the students made mandatory
permissions should be given.

Madhu
Signature of Alumni

S.M

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)

Employer Feedback - Academic Year: 2018-19

Name of the Company/Institute: **TS TECHNOLOGIES**
 Name of the Evaluating Graduate with Designation: **TECHNICAL DIRECTOR**
 Year: **2019**

Employers are the important stakeholders to our institution. Your feedback facilitates to correct and improve various existing processes in the institution. On behalf of the institute, we request you to spare your valuable time to give your feedback in the following format.

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
-------------	-------------	--------	-----------	--------

S.No	Parameters	Rating (5 being highest)				
1	Domain Knowledge and Aptitude Levels	3				
2	Problem analysis and design of appropriate solutions	4				
3	Attitude towards Research based approach	5				
4	Adaptability to new technology/tools and zeal to be a constant learner	5				
5	Commitment to work, managerial skills and ability to meet deadlines	4				
6	Work towards sustainable development, societal improvements and Environmental Benefits	3				
7	Professional ethics	3				
8	Communication Skills	4				
9	Team spirit, interpersonal relations and leadership skills	3				
10	How do you rate capability to analyze, synthesize, design, develop and test systems/processes?	4				
11	Overall Job performance	3				

Any other relevant information : I Recommended to add the latest developments such as permanent magnet axial-flow machines in special electrical machines

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P. Chinn
Signature of the Employer



Dept. of Electrical Engineering
 Chaitanya Bharathi Institute of Technology

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)

Employer Feedback - Academic Year: 2018-19

Name of the Company/Institute: **IMRC**

Name of the Evaluating Graduate with Designation: **Dr. P. C. Reddy**

Year: **2019**

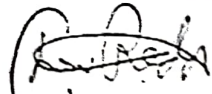
Employers are the important stakeholders to our institution. Your feedback facilitates to correct and improve various existing processes in the institution. On behalf of the institute, we request you to spare your valuable time to give your feedback in the following format.

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
-------------	-------------	--------	-----------	--------

S.No	Parameters	Rating (5 being highest)				
1	Domain Knowledge and Aptitude Levels					
2	Problem analysis and design of appropriate solutions					
3	Attitude towards Research based approach					
4	Adaptability to new technology/tools and zeal to be a constant learner					
5	Commitment to work, managerial skills and ability to meet deadlines					
6	Work towards sustainable development, societal improvements and Environmental Benefits					
7	Professional ethics					
8	Communication Skills					
9	Team spirit, interpersonal relations and leadership skills					
10	How do you rate capability to analyze, synthesize, design, develop and test systems/processes?					
11	Overall Job performance					

Any other relevant information :

1. It is recommended to add a course on Industrial Systems, Components.



Signature of the Employer
Managing Director
INTERLEAVED MULTIDISCIPLINARY
RESEARCH CENTRE
Hyderabad

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY(A)

Employer Feedback - Academic Year: 2018-19

Name of the Company/Institute: Medha Servo Motor Drives
 Name of the Evaluating Graduate with Designation: Venkatesh
 Year: 2018

Employers are the important stakeholders to our institution. Your feedback facilitates to correct and improve various existing processes in the institution. On behalf of the institute, we request you to spare your valuable time to give your feedback in the following format.

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
-------------	-------------	--------	-----------	--------

S.No	Parameters	Rating (5 being highest)				
		5	4	3	2	1
1	Domain Knowledge and Aptitude Levels	4				
2	Problem analysis and design of appropriate solutions	4				
3	Attitude towards Research based approach	3				
4	Adaptability to new technology/tools and zeal to be a constant learner	4				
5	Commitment to work, managerial skills and ability to meet deadlines	4				
6	Work towards sustainable development, societal improvements and Environmental Benefits	5				
7	Professional ethics	4				
8	Communication Skills	4				
9	Team spirit, interpersonal relations and leadership skills	3				
10	How do you rate capability to analyze, synthesize, design, develop and test systems/processes?	5				
11	Overall Job performance	4				

Any other relevant information :

1. I suggest you to incorporate Electrical M/c Design to get students.

a concept of mathematical modelling of Electrical machines.


 Signature of the Employer

2. Various transducers may be added in Electrical measurement & Instrumentation course.

V N

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

FACULTY FEEDBACK ON CURRICULUM - Academic Year: 2018-19

Name of the Faculty Member : D. HARSHA

Department: EEE

Designation :

ASST. PROF.

Courses Handled :

EC-II, EM-I LAB, LIC LAB, MINI PROJECT, LCS, LCS LAB, DSP LAB

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
-------------	-------------	--------	-----------	--------

S.No	Parameters	Rating (5 being highest)				
1	The design of the curriculum addresses the holistic development of student.	✓	✓	✓	✓	
2	The curriculum is well balanced with knowledge, skills and employability.	✓	✓	✓	✓	
3	Suitability of the Syllabus to the Course.	✓	✓	✓	✓	✓
4	The course/courses are relevant to the present scenario.	✓	✓	✓	✓	
5	Course objectives and outcomes are well defined.	✓	✓	✓	✓	✓
6	Prescribed books/suggested readings and other references appropriate.	✓	✓	✓	✓	✓
7	BoS members from Academia and Industry constructive in updating the syllabi according to the changing educational challenges and requirements in line with regulating bodies like AICTE, UGC etc.	✓	✓	✓	✓	✓
8	The scheme and evaluation schedules satisfy the Teaching Learning Process.	✓	✓	✓	✓	
9	Freedom to suggest/propose/modify/incorporate new topics in the syllabus during the revision of curriculum?	✓	✓	✓	✓	✓
10	Institute/Department gives the freedom to adopt new technologies/strategies of innovative teaching?	✓	✓	✓	✓	
11	The environment in the department is conducive to learning, teaching, and research.	✓	✓	✓	✓	
12	Provisions/ for professional development.	✓	✓	✓		
13	Adequacy of infrastructure (class/staff rooms, labs, library, and ICT facilities) in the institute.	✓	✓	✓	✓	

Any other relevant information :

The concept of Fourier series & Fourier Transform are removed as it is available in the SDS course, some of the topics of Laplace transform are removed as left in signals & system course.

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Signature of the faculty

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

FACULTY FEEDBACK ON CURRICULUM - Academic Year: 2018-19

Name of the Faculty Member : C. Harish
 Designation : Asst. Prof.
 Courses Handled : DE Theory, LQE-lab.

Department: EEE

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
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S.No	Parameters	Rating (5 being highest)				
1	The design of the curriculum addresses the holistic development of student.	4				
2	The curriculum is well balanced with knowledge, skills and employability.	4				
3	Suitability of the Syllabus to the Course.	4				
4	The course/courses are relevant to the present scenario.	4				
5	Course objectives and outcomes are well defined.	5				
6	Prescribed books/suggested readings and other references appropriate.	5				
7	BoS members from Academia and Industry constructive in updating the syllabi according to the changing educational challenges and requirements in line with regulating bodies like AICTE, UGC etc.	4				
8	The scheme and evaluation schedules satisfy the Teaching Learning Process.	4				
9	Freedom to suggest/propose/modify/incorporate new topics in the syllabus during the revision of curriculum?	4				
10	Institute/Department gives the freedom to adopt new technologies/strategies of innovative teaching?	4				
11	The environment in the department is conducive to learning, teaching, and research.	3				
12	Provisions/ for professional development.	4				
13	Adequacy of infrastructure (class/staff rooms, labs, library, and ICT facilities) in the institute.	5				

Any other relevant information :

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 Signature of the faculty

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS), HYDERABAD-75

FACULTY FEEDBACK ON CURRICULUM - Academic Year: 2018-19

Name of the Faculty Member : M. SHROPHANATH
 Designation : Asst. Professor
 Courses Handled : PS LAB

Department: EEE

Excellent-5	Very Good-4	Good-3	Average-2	Poor-1
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S.No	Parameters	Rating (5 being highest)				
		5	4	3	2	1
1	The design of the curriculum addresses the holistic development of student.		4			
2	The curriculum is well balanced with knowledge, skills and employability.		4			
3	Suitability of the Syllabus to the Course.	5				
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7	BoS members from Academia and Industry constructive in updating the syllabi according to the changing educational challenges and requirements in line with regulating bodies like AICTE, UGC etc.		4			
8	The scheme and evaluation schedules satisfy the Teaching Learning Process.		4			
9	Freedom to suggest/propose/modify/incorporate new topics in the syllabus during the revision of curriculum?	5				
10	Institute/Department gives the freedom to adopt new technologies/strategies of innovative teaching?	5				
11	The environment in the department is conducive to learning, teaching, and research.		4			
12	Provisions/ for professional development.		4			
13	Adequacy of infrastructure (class/staff rooms, labs, library, and ICT facilities) in the institute.		4			

Any other relevant information :

Suggested to add experiments on various types of Relays as hardware experiments.

Signature of the faculty

HEAD
 DEPARTMENT OF EEE
 CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY