

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A), HYDERABAD DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING COURSE EXPERT GROUP (CEG) ME (ES&VLSID)

Curriculum Change from AICTE prescribed Model Curriculum (R-19) to AICTE prescribed Model Curriculum (R-20)

Objective:

To contribute to the effective implementation of pedagogical approaches and assessment tools identified by PAQIC, DAB and other regulatory bodies.

Composition:

Course experts from the specified set of courses chaired by senior faculty among them.

Meeting Frequency: Min. 3 times in a semester.

Beginning of the semester, after the first Class Test, after external examinations results are published (for results analysis, computation of CO attainment) and also whenever required.

Tenure: Three years

Quorum: 60%

Roles and Responsibilities:

- 1. Defining, Reviewing and Reframing the Course Outcomes based on Blooms Taxonomy for all the specified Courses
- 2. Framing of Course Articulation matrix (CO-PO mapping)
- 3. Computation of CO attainments
- 4. Design of assessment tools suitable for the specified courses, Results analysis and preparing the action plan for improvement of Results
- 5. Conducting Course End Survey, analysis and identify actions to be taken for improving pedagogical approaches
- 6. Identifying Curriculum gaps by observing CO-PO attainment levels of previous batches
- 7. Question paper validation to ensure the desired standard from outcome attainment perspective as well as learning levels perspective
- 8. Setting course wise CO attainment targets before the beginning of the semester
- 9. Maintenance of Course Files
- 10. Preparing Action plans for improvement and corrective measures

11. Prepare and submit periodic reports (Before the commencement and at the end of each semester) to the Program Assessment Quality Improvement Committee (PAQIC) about the activities carried out

Deliverables:

Submit Reports to PAQIC on Effectiveness of TLP in terms of:

- 1. Revision of Course Outcomes
- 2. Course Outcome attainment
- 3. Gaps identified, actions taken and their effectiveness
- 4. Setting course wise CO attainment targets
- 5. Suggestions for subsequent Curriculum Revision

I. CEG- 1: Embedded Systems

S.no	Semester	Course Code	Name of the Course	Course Experts Group	
1.	I/II	20EC C202	Embedded System Design using RTOS		
2.	I/II	20EC C203	Micro controllers and Programmable Digital Signal Processors	D DC	
3.	I/II	20EC C206	Microcontrollers and Programmable Digital Signal Processors Lab	Dr. PS (Chairman), Smt.NDL,	
4.	I/II/III	20EC E201	Advanced Computer Organization	Sri.GMR,	
5.	1/11/111	20EC E202	Communication Buses and Interfaces	Sri.MZJ, Smt.JM, and Smt.DS	
6.	I/II/III	20EC E208	Programming Languages for Embedded Software		
7.	1/11/111	20EC E211	System Design with Embedded Linux		

II. CEG 2: VLSI

S. no	Semester	Course Code	Name of the Course	Course Experts Group	
1.	1. I/II	20EC C201	Analog and Digital CMOS VLSI		
1.	1/11	20EC C201	Design		
2	1 /11	20EC C205	Analog and Digital CMOS VLSI		
2.	I/II	20EC C205	Design Lab		
2	1 /11	20EC C207	RTL Simulation and Synthesis with		
3.	I/II	20EC C207	PLDs Lab		
4	I/II	20EC C204	VLSI Design Verification and	Dr.MLNC(Chairman), Dr. MRR, Sri. AKK,	
4.			Testing		
_	I/II	20EC C208	RTOS and VLSI Design Verification		
5.			Lab		
6.	I/II/III	20EC E203	Data Acquisition System Design	Sri.NJMR , Smt.BN, Sri.PCS, Sri.MZJ,	
7.	I/II/III	20EC E204	FPGA & CPLD Architectures	Sri.ECS, and Smt.GC	
8.	I/II/III	20EC E205	Low Power VLSI Design	ornizos, and omicide	
0	I/II/III	1/11/111 2	I/II/III 20EC E206 Nano-n	Nano-materials and	
9.			I/II/III ZUEC EZUO	Nanotechnology	
10.	I/II/III	20EC E209	RF IC Design		
11.	I/II/III	20EC E210	SoC Design		
12.	I/II/III	20EC E212	VLSI Signal Processing		
13.	I/II/III	/II/III 20EC E213	VLSI Technology and Physical]	
			Design Automation		

III: CEG-3: Projects & Seminars

S.no	Semester	Course Code	Name of the Course	Course Experts
1.	II	20EC C209	Mini Project with Seminar	Dr.NVKR
2.	III	20EC C210	Dissertation / Phase-I	(Chairman),
3.	IV	20EC C211	Dissertation / Phase-II	Dr.MRR, Dr.PS, Sri.PCS and Sri.MZJ

IV: CEG- 4: Miscellaneous

S.no	Semester	Course Code	Name of the Course	Course Experts
1.	I/II/III	20EC E207	Network Security and	
			Cryptography	Dr PNS
2.	I/II/III	20EC E109	Pattern Recognition and	(Chairman), Dr. AV, Smt.AS,
			Machine Learning	Sri.GVPK and
3.	I/II/III	20EC E214	Wireless Sensor Networks	Smt.DND
4.	I/II	20EC A101	Value Education	