

Name of Faculty Smt. Ghata Chauhan
 Designation Assistant Professor
 Nature of Job/Appointment Contract
 Date of Joining 24 - 06 - 2019
 E-mail ghatachauhan_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
PG	M. Tech. (VLSI Design)	Distinction
UG	B. Tech. (ECE)	Distinction

Work Experience

Teaching	6+ Years
Research	--
Industry	--
Others	--
Area of Specialization	VLSI, IoT, Blockchain
Professional Memberships	Member, International Association of Engineers - IAENG Number: 327102
Responsibilities held at Institution Level	1. Member Green Audit Committee
Responsibilities held at Department Level	1. All feedbacks including Exit Survey 2. Department Coordinator CAMU 3. Coding school & AWS Faculty Coordinator
Research Guidance	--
Awards Received	--
Courses Handled at Under Graduate / Post Graduate Level.	IoT applications, Blockchain Technology, VLSI Technology, VLSI Design, IoT, Digital electronics, Electronics Devices, Basic Electronics
No. of Papers Published	National Journals – -- International Journals – 01 National Conference – -- International Conference – --
Projects Carried out	--
Patents	--
Technology Transfer	--
Invited Speaker	--
No. of Books/Chapter Published with details	--
Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops.Other Trainings (Attended and/or Organized).	<ol style="list-style-type: none"> 1. Successfully completed Teacher Training Program of 8 weeks on "Module 5_ Technology Enabled Learning & Life Long Self Learning" of NITTT in Feb 2023. 2. Successfully completed Teacher Training Program of 8 weeks on "Module 6_ Student Assessment and Evaluation" of NITTT in Feb 2023. 3. Attended a 5 days live online workshop on "Blockchain Technology and its applications" organized by EduxLabs in association with Mechanica IIT Madras from 07 Nov to 11 Nov, 2022. 4. Attended a 5-day online FDP on "Inculcating Universal Human Values in Technical Education" organized by All India Council for Technical Education (AICTE) from 16 August, 2021 to 20 August, 2021. 5. Attended an online non-credit course of 5 weeks on Blockchain: Foundations and Use Cases Authorized by ConsenSys Academy and offered through Coursera.

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6. Attended a Five Day Online STTP on “Blockchain for Digital Transformation” conducted by Department of CSE, CBIT(A), Hyderabad, Telangana from 12 August to 16 August, 2021.
7. Attended an Online Elementary FDP "Perspectives of Blockchain Integrated with IoT: Challenges and Opportunities" PSNA College of Engineering and Technology from 05 July, 2021 to 09 July, 2021.
8. Completed Teacher Training Program of Module-2: Professional Ethics and Sustainability conducted by NITTT, AICTE for 8 weeks, from 01 Sep 2021 to 31 Oct 2021.
9. Completed Teacher Training Program of “Module 1_ Orientation Towards Technical Education and Curriculum Aspects” conducted by NITTT, AICTE for 8 weeks from 01 Sep 2020 to 31 Oct 2020.
10. Completed online certification skill development program on “Design, Implementation and Verification in VLSI” organized by Sandeepani School of embedded system design during 27-04-2020 to 01-05-2020.
11. Successfully completed 4-day Hands-on Workshop on “Effective and Efficient Online Teaching in the Age of Corona” organized by IIT Bombay Mneict conducted during 21-05-2020 to 24-05-2020
12. Completed five Days Online Faculty Development Program on “RECENT COMMUNICATION TOOLS AND ITS APPLICATION” Organized by Department of Electronics and Communication Engineering, St. Martin’s Engineering College during 19-05-2020 to 23-05-2020.
13. Completed Two-day FDP on “Effective Technical Report Writing Using LaTeX” organized by Dept. of ECE, MGIT, Hyderabad during 08-06-2020 and 09-06-2020
14. Successfully completed one week Faculty Development Program on “OUTCOME BASED EDUCATION AND NBA ACCREDITATION PROCESS” organized by CBIT, Hyderabad during 28-05-2020 – 01-06-2020.
15. Participated in the IIC Online Sessions conducted by Institution's Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 nationwide lockdown.
16. Attended Webinar on “Trends and Technologies in VLSI” conducted by Pantech E learning on 19th May 2020.
17. Successfully completed 5 day Faculty Development Program conducted by ATAL Training Program, NITTTR Chandigarh on “Quantum Computing” during 15 - 19 June 2020.
18. Attended India's First Leadership Talk by “Mr. Shridhar Venkat, (CEO) Akashaya Patra Foundation” organized by MHRD’s Innovation Cell on 20th June, 2020.
19. Successfully completed Hands-on Workshop on “BodhiTree/SAFE workshop” organized by IIT Bombay Mneict conducted on 20-06-2020
20. Successfully completed One Week Online Faculty Development Program on "Research Opportunities in Image Processing and Machine Learning", conducted by the Department of Electronics and Communication Engineering during 29th June to 03rd July 2020.

Details of Journal Publications/
Conferences (National and
International)

International Journal:

1. Shivani Saxena, Anjali Tiwari, **Ghata Chauhan**, Nidhi, "A Comparative Study of Leakage Reduction Techniques Used In FPGA for Optimized Area and Power Consumption." Int. Journal of Engineering Research and Applications, Vol 4, issue 2 (Version 1), February 2014, pp. 89-94.



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