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Details of Journal Publications/

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Conferences

International)

International Conference :

- 1. Jahangir, Mohd Ziauddin, and Chandra Sekhar Paidimarry. "Design of a Novel Charge Pump based Current Starved Ring Oscillator with Reduced Phase Noise." 2023 International Conference for Advancement in Technology (ICONAT). IEEE, 2023.
- Jahangir, Mohd Ziauddin, et al. "Design and Implementation of FPGA based DDS-ADPLL for Resonant Frequency Tracking in Sensors." 2022 IEEE International Conference on Electronics, Computing and Communication Technologies (CONECCT). IEEE, 2022.
- Quadri, Syed Ali Imran, and Mohd Ziauddin Jahangir. "Design, Implementation and Performance Comparison of Different Branch Predictors on Pipelined-CPU." 2017 International Conference on Computer, Electrical & Communication Engineering (ICCECE). IEEE, 2017.
- 4. Mounika, J., K. Ramanujam, and Mohd Ziauddin Jahangir. "CMOS based design and simulation of ternary full adder and Ternary coded Decimal (TCD) adder circuit." 2016 International Conference on Circuit, Power and Computing Technologies (ICCPCT). IEEE, 2016.
- Jahangir, Mohd Ziauddin, and K. Venkata Narasimha. "Design of a new ternary SRAM cell (ZV-SRAM) based on innovative level shift based ternary inverter (ZV-Inverter)." 2015 Annual IEEE India Conference (INDICON). IEEE, 2015.
- 6. Jahangir, Mohd Ziauddin, P. Chandrasekhar, and NV Koteswara Rao. "Design and simulation of programmable band-gap reference circuit." 2015 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics (PrimeAsia). IEEE, 2015.
- Jahangir, Mohd Ziauddin, and Chandra Sekhar Paidimarry. "Design of programmable Op-Amps with minimized DC variations at output." 2013 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics (PrimeAsia). IEEE, 2013.

National /International Journal :

- 1. Jahangir, Mohd Ziauddin, and Paidimarry Chandra Sekhar. "Design of novel hybrid-digitally controlled oscillator for ADPLL." Memories-Materials, Devices, Circuits and Systems (2023): 100052. Elsevier
- Jahangir, Mohd Ziauddin, and J. Mounika. "Design and simulation of an innovative CMOS ternary 3 to 1 multiplexer and the design of ternary half adder using ternary 3 to 1 multiplexer." Microelectronics Journal 90 (2019): 82-87. Elsevier.
- Jahangir, Mohd Ziauddin, et al. "Design of an All Digital Phase-Locked Loop Using Cordic Algorithm." Advances in Signal Processing and Communication Engineering: Select Proceedings of ICASPACE 2021. Singapore: Springer Nature Singapore, 2022. 143-149.
- 4. Jahangir, Mohd Ziauddin and Paidimarry Chandra Sekhar. "The Design of DDS ADPLL using ARM Micro Controller." IJRASET 2022.

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