

Name of Faculty Dr. S. Radha
Designation Associate Professor
Nature of Job/Appointment Regular

Date of Joining 12 - 12 - 2022

E-mail radhas_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (ECE)	Awarded
PG	M. Tech. (VLSD)	First class with Distinction (Topper-Gold medalist)
UG	B. Tech. (ECE)	Distinction

Work Experience
Teaching 14.5 Years
Research 9.5 years

Area of Specialization
Green Communication Networks with emphasis on optimization techniques and multi-layer approaches for WSN. VLSI architectures for low power applications. Signal and Image processing algorithms for EEG and brain related diseases

Academic Identity
Scopus ID: 24344889500, Web of Science Researcher ID: F-2074-2018,
URL-of-Scopus: <https://www.scopus.com/authid/detail.uri?authorId=24344889500>
URL of WoS: <https://publons.com/researcher/3632847/s-radha/>
ORCID ID: 0000-0003-4817-3683, Google Scholar ID: 6SEdy_AAAAAJ&hl ; Scopus h-index: 5, Scopus Citations: 139, **Sci indexed: Cumulative Impact Factor (CIF) – 14.281**
Google Scholar h-index: 6, Google Scholar i10-index: 2, Google Scholar citations: 203
NME-ICT-Vidwan [Profile URL: https://vidwan.inflibnet.ac.in/profile/232597/MjMyNTk3](https://vidwan.inflibnet.ac.in/profile/232597/MjMyNTk3)
Google-Scholar-URL: https://scholar.google.com/citations?user=6SEdy_AAAAAJ&hl
Linked-in-URL <https://www.linkedin.com/in/radha-subramanyam-a510b0218/>
Skype ID: nagabushanammooc@gmail.com
Research gate URL: <https://www.researchgate.net/profile/Radha-Subramanyam>

Professional Memberships
1.The Society of Digital Information and Wireless Communications (SDIWC)– Id: **11444**
2.International Association of Engineers (IAENG)- Id: **141006**
3.International Association of Computer Science and Information Technology (IACSIT)
4.International Society for Research and Development (ISRDR)- Id: **M4150900841**

Responsibilities held at Institution Level
1. **CBIT** - Co-Convenor in Technical Committee (**Sudhee 2023** – Jan & Feb 2023), One of the Editor of Sudhee 2023 Proceedings Book with **ISBN Number: 978-93-91679-81-1**
2. **Subject Expert Category** – Incharge for preparing questions of general aptitude for **Karunya Entrance Examination (KEE) 2022** conducted by Karunya University (KU), Coimbatore, Tamilnadu in May 2022.

3. Subject Expert Category – Incharge for preparing questions of general aptitude for **Karunya Entrance Examination (KEE)2021** conducted by Karunya University (KU), Coimbatore in June 2021.

Program Officer- NSS (one unit) for more than 5 years in Karunya University (KU), CBE, TN

Responsibilities held at Department Level

1. CBIT - **Time table Co-ordinator** (Dec 2022 – till date)
2. KU- **GATE** (Graduate Aptitude Test for Engineering) exam **Coaching- Co-ordinator** (Jul 2013-Nov 2022)
3. KU- UG 4th Year **Project Co-ordinator** (Jul 2013-Jun 2018)
4. KU- **Time table Co-ordinator** (Jun 2013-Nov 2022)
5. KU- Worked for two **NBA Accreditations**-once in 2019 & second Accreditation in 2022 (Data on faculty Publications)
6. KU- Worked for **NAAC Accreditation**- 2021 & 2022 (Data on Career Guidance and Competitive exams)
7. KU- Editorial Team- IEEE Sponsored International Conferences **ICSIPR'13** on 7th -8th Feb, 2013 & **ICSPC'17** on 28th-29th Jul 2017.
8. TKRCET, Hyd- Worked for two **NBA Accreditations**-once in 2008 & second Accreditation in 2012

Research Guidance

M. Tech - Guided 8 projects

Awards Received

1. **Topper in M. Tech** and received gold medal from college chairman in 2011.
2. **NSS State award**, Tamilnadu- Program officer for more than 5 years.
3. Qualified in **UGC-NET Jun 2020** (Electronics Science) conducted by NTA (National Testing Agency) in Nov 2020.
4. Qualified in **UGC – NET Dec 2019**(Electronics Science) conducted by NTA (National Testing Agency), India
5. Qualified in **FET-2011**(Faculty Eligibility Test-Conducted by JNTU, Hyderabad).
6. Qualified in **GATE 2019** (conducted by **IIT, Madras**) with score 415 validity 17 March 2019 to 16 March 2022.
7. Qualified in **GATE 2015** (conducted by **IIT, Kanpur**) with score 413 validity 27 March 2015 to 26 March 2018.
8. Qualified in **GATE 2013** (conducted by **IIT, Bombay**) with score 449 validity 15 March 2013 to 14 March 2014 and **GATE 2013** with score 533 validity 15 March 2014 to 14 March 2015.
9. Qualified in **GATE 2012** (conducted by **IIT, Delhi**) with 482 score validity 15 March 2012 to 14 March 2014.
10. Qualified in **GATE 2009**(conducted by **IIT, Roorkee**) with **95%** score validity 15 March 2009 to 14 March 2010 and completed M.Tech in VLSI SYSTEM DESIGN with this GATE Score. Received **MHRD Scholarship of 8000/- per month** for a period of 24 months.
11. Qualified in **GATE 2008** (conducted by **IISc, Bangalore**) with **87%** score validity 15 March 2008 to 14 March 2010.
12. Qualified in **BEML (Govt. Sector)** Written Exam (**AIR-88**) for the post of **Executive Trainee (ET)** in 2011.
13. Selected for Interview in **OCES-DGFS BARC** (Bhabha Atomic Research Center) based on 2009 GATE Score.
14. Qualified in **ISRO** Written Exam (**AIR-103**) for the post of **Scientist-B** in 2011.
15. Qualified in **ISRO** Written Exam for the post of **Scientist-B** in 2008.
16. **Topper in SSC** (among all schools in Bangaru Palyam mandal, Chittoor (Dt), A.P.) with 521 marks in the year 1999.
17. Qualified in **Jawahar Navodaya Vidyalaya Entrance Exam** and completed class VI-IX education (in the year 1994 - 1998) from **Jawahar Navodaya Vidyalaya, Valasapalli**, Near MadanaPalli, Chittoor (Dt), A.P. under **Jawahar Navodaya Vidyalaya**

Samithi. Based on merit - Complete education, sports, amenities are fully **sponsored by central government.**

18. Handball Game (From Jawahar Navodaya Vidyalaya) - Participated and won 1st, 2nd place in XXVI, XXVII, XXVIII **A.P. State Rural Sports Tournament** and VIII **National Meet** for Group 'A' Games, **Agra**(Uttar Pradesh), XLIII **National School Games, Bhopal** (Madhya Pradesh) during the period **1995 to 1998.**

Courses Handled at Under Graduate / Post Graduate Level

Control systems, Signals and systems, Switching Theory and Logic Design / Digital Electronics, Analog Communications, Digital Communications, Linear IC Applications, Electronic circuits, DSP Architectures and Processors, Communication systems, Electronic Devices and Circuits, Pulse and Digital Circuits, Modern Digital Communications, Information Coding Theory, Basic Electronics

No. of Papers Published	National Journals – nil	International Journals – 15 (Sci indexed- 8 , Scopus indexed- 4, Non Scopus / UGC Listed- 3)
Projects Carried out	National Conference – 12	International Conference – 32 (IEEE Scopus indexed- 24)
Patents		--
Technology Transfer		--
Invited Speaker (Reviewer)	<p><u>Reviewer in Journals:</u></p> <ol style="list-style-type: none"> 1. International Journal of Advances in Engineering Sciences and Applied Mathematics (scopus journal, Hindwai Publishers) 2. Frontiers in Neurorobotics (Sci Indexed Journal, Impact Factor – 3.493) 3. Engineering Optimization (Sci indexed Journal, Impact Factor- 3.230, Taylor & Francis) 4. Frontiers in Neuro informatics (Sci Indexed Journal, Impact Factor – 4.081) 5. Automatika: Journal for Control, Measurement, Electronics, Computing and Communications (Sci Indexed Journal, Impact Factor – 1.156, Taylor & Francis journal pub) 6. Journal of Circuits, Systems, and Computers (Sci Indexed Journal, Impact Factor(IF) – 1.333, world scientific journal pub) 7. Circuit World (Sci Indexed Journal, IF – 0.875, Emerald Pub.) 8. Transactions on Emerging Telecommunications Technologies (Sci Indexed Journal, Impact Factor – 1.258, John Wiley) 9. International Journal of Intelligent Information Technologies (IJIT) – Scopus Indexed 10. Intelligent Decision Technologies: an International Journal – Scopus Indexed 11. International Journal of Sensors, Wireless Communications and Control (SWCC)-Scopus Indexed 12. International Journal of Artificial Intelligence and Machine Learning (IJAIML)-Non Scopus Indexed <p><u>Resource Person:</u></p> <ol style="list-style-type: none"> 1. Training Programme “Machine Learning and its applications using python programming (MLAPP 2020)” conducted by karunya university from 27th May 2020 to 6th Jun 2020 2. Reviewer and session chair in ICSPP 2021 Conference conducted on 13th, 14th May 2021 in KITS, Coimbatore. 	

3. **Reviewer** in **ICCDF 2021** Conference conducted on 3rd, 4th Apr 2021 in Northcap University, Gurugram

No. of Books/Chapter Published with details

1. Dr. M. V. S. Murali Krishna, Dr. Sugandha Singh, Dr. Swatmaram, Dr. T. Murali Krishna, Dr. T. Sudhakar Babu, **Dr. S. Radha** “Proceedings of Sudhee 2023 (Nurture Natural Technologies)” a National Level Technical Fest conducted on 31st Jan – 1st Feb 2023 by CBIT, Hyderabad. Book- **ISBN Number: 978-93-91679-81-1**
2. **Radha, S.**, Sachin, B., Pourmoafi, S., & Nagabushanam, P. (2022). FRAME Routing to Handle Security Attacks in Wireless Sensor Networks. In *Security Analytics* (pp. 165-189). Chapman and Hall/CRC. **Book Chapter-** Taylor&Francis
3. P. Nagabushanam, S. Thomas George, D.S. Shylu, **S.Radha** “GABOR FILTER BASED TONSILLITIS ANALYSIS USING VHDL” presented in **Springer** -International Conference on Artificial Intelligence, Smart Grid and Smart City Applications (AISGSC 2019) organized by PSG college of Technology , CBE on 3rd to 5th Jan 2019- **Book chapter** edition: 2020
 - o ISBN 978-3-030-24050-9 ISBN 978-3-030-24051-6 (eBook) <https://doi.org/10.1007/978-3-030-24051-6>

Contributed Educational e-content

Youtube Channel Link (Educational e-content):

<https://www.youtube.com/channel/UCrcsTiUiiXHj2X98tJQKqjQ> (click on **videos** button to view all the uploaded videos)

Number of Videos uploaded: 411

Number of Subscribers: 169

Number of views: 31,870

Sample videos link below:

Subject: Signals & Systems

Topic: Discrete time fourier series (DTFS)

https://www.youtube.com/watch?v=V3klIY_vdqQ&t=27s

Topic: GATE exam problems on Laplace transform

<https://www.youtube.com/watch?v=URGIIL6DTRY&t=28s>

Subject: General Aptitude

Topic: Boats and streams

<https://www.youtube.com/watch?v=iozbAbBscz8&t=41s>

Topic: Pipes and cisterns

https://www.youtube.com/watch?v=U8jlkBVnY_Y&t=30s

Subject: VLSI Design Lab

Topic: Layout of NAND GATE in Microwind software

<https://www.youtube.com/watch?v=UV4lp3tQuVU&t=65s>

Topic: 3 bit Asynchronous Up Counter in LT Spice software

<https://www.youtube.com/watch?v=nU-d0q-NcxU&t=4s>

Subject: Digital Electronics

Topic: GATE exam Coaching – Problems on flipflops

<https://www.youtube.com/watch?v=iIUmPbgBizU&t=7s>

Topic: IES exam Coaching – Problems on flipflops

<https://www.youtube.com/watch?v=jGyN7wNGs8Q>

Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops/Other Trainings (**Attended and/or Organized**).

Organized: 19 ; Attended: 69

NPTEL Courses

12 Completed

1. NPTEL Online course “Accreditation and Outcome based Learning (**OBE**)” during Aug – Oct 2022 with **8 assignments** conducted by **IIT Kharagpur (Elite)**.
2. NPTEL Online course on “**NBA Accreditation** and Teaching-Learning in Engineering (NATE)” during Jan 2021 – Apr 2021 with **12 assignments** conducted by **IISC Bangalore (Silver Medal)**.

3. NPTEL Online course on “Deep Learning” during Jul 2019 – Nov 2019 with **12 assignments** conducted by **IIT Madras (Elite)**.
4. NPTEL Online course on “Switching Circuits and Logic Design” during Jul 2018 – Oct 2018 with 12 assignments conducted by **IIT Madras (Elite)**.
5. NPTEL Online course on “Control Engineering” during Jul 2017 – Oct 2017 with **12 assignments** conducted by **IIT Madras (Elite)**
6. NPTEL Online course on “Medical Image Analysis” during Feb 2017 – Mar 2017 with **4 assignments** conducted by **IIT Kharagpur (Elite)**.
7. NPTEL Online course on “Wireless Ad hoc and Sensor Networks” during Jan 2017 – Mar 2017 with **12 assignments** conducted by **IIT Madras (Elite)**
8. NPTEL Online course on “Discrete time signal processing” during Jul 2016 – Nov 2016 with **12 assignments** conducted by **IIT Madras (Elite)**
9. NPTEL Online course on “Networks and Systems” during Jul 2015 – Nov 2015 with **12 assignments** conducted by **IIT Madras (Elite)**
10. NPTEL Online course on “Probability and Random Variables/Processes for Wireless Communications” during Sep 2015 – Nov 2015 with **4 assignments** conducted by **IIT Kanpur (Elite)**
11. NPTEL Online course on “Principles of Modern CDMA/MIMO/OFDM Wireless Communications” during Jul 2015 – Aug 2015 with **8 assignments** conducted by **IIT Kanpur (Elite)**
12. NPTEL Online course on “Basic Electrical Circuits Jul 2015 – Aug 2015 with **8 assignments** conducted by **IIT Madras**.

ATAL Courses – 5 Completed

1. ATAL workshop on “Modulation, coding and multiple access techniques for wireless communication and storage systems” during 15th Nov to 19th Nov 2021 conducted by IIT, Goa.
2. ATAL workshop on “Signal Processing for 5G- Algorithms and Implementation” during 9th Aug to 13th Aug 2021 conducted by IIT, Goa.
3. ATAL workshop on “Sensors Technology” during 8th Feb 2021 to 12th Feb 2021 conducted by North-Eastern Hill University, Shillong.
4. ATAL workshop on “Internet of Things (IoT)” during 11th Jan 2021 to 15th Jan 2021 conducted by LBS College of Engineering, Kasaragod, Kerala.
5. ATAL workshop on “Sensors Technology” during 7th Dec 2020 to 11th Dec 2020 conducted by Thiagarajar Polytechnic college, Salem, Tamilnadu

Details of Journal Publications/
Conferences (National and International) **Total Scopus publications - 36**

International Journals:

Sci Indexed/ Web of Science Publications:

1. **Radha, S.**, Sachin, B., Pourmoafi, S., & Nagabushanam, P. (2022)., “Distributed MAC Protocol with Game Theory Optimization for Wireless Sensor Networks”, Ad Hoc & Sensor Wireless Networks, Volume 54, Issue 3-4, Pages 291 – 326, **Dec 2022** (ISSN: 15519899) (**Impact Factor- 0.4, Old City Publishers**)
DoI: 10.32908/ahsw.n.v54.8141
2. **Radha, S. ***, G.Josemin Bala, Nagabushanam, P., “Multilayer DS-MAC with Game theory Optimization”, Circuit World- July 2021. (ISSN:0305-6120) (**Impact Factor – 1.027, Emerald Publishers**), DOI (10.1108/CW-08-2020-0197)
3. **Radha, S. ***, G.Josemin Bala, Nagabushanam, P., E. Grace Mary Kanaga, “Energy Efficient MAC with Variable Duty Cycle for Wireless Sensor Networks”, International Journal of Electronics,109(3), pp. 367–390, Feb 2021 (**Impact Factor – 1.457, Taylor and Francis**)- DOI: 10.1080/00207217.2021.1892202, SJR-0.26
4. **Radha, S. ***, G. Josemin Bala, Nagabushanam, P., “FRAME routing with Game theory Optimization for wireless networks”, International Journal of Communication systems, Vol 34, Issue 6, Apr 2021. (ISSN: 1099-1131) (**Impact Factor – 1.882, John wiley**)
5. Nagabushanam, P., Thomas George, S., **Radha, S.**, “EEG signal classification using LSTM and improved neural network algorithms”, Soft Comput 24, 9981–10003, July 2020. (ISSN:1433-7479) (**Impact Factor – 3.732, Springer**)
6. Nagabushanam, P., George, S. T., S. Raveena Judie Dolly, D., **Radha, S.***, “Artifact Cleaning of Motor Imagery EEG by Statistical Features Extraction using wavelet Families”, International Journal of Circuit Theory and Applications, 2219-2241, Vol 48, Issue 12, 2020. (ISSN: 1097-007X) (**Impact Factor – 2.378, John wiley**)

7. Vimukth; sam, shylu; **Radha, S.**; Paul, P.Sam; Samuel, Joel, "Design of a power efficient Kogge-Stone Adder by exploring new OR gate in 45nm CMOS Process" *Circuit World*, 46(4), pp. 257–269, Mar 2020.(ISSN:0305-6120) (**Impact Factor – 1.027, Emerald**)
8. **Radha, S.** *,Shylu, D.S., Nagabushanam, P., "Power efficient low latency architecture for decoder: Breaking the ACS bottleneck", *International Journal of Circuit Theory and Applications*, 1513–1528, Vol 47, Issue 9, 2019.(ISSN: 1097-007X) (**Impact Factor – 2.378, John wiley**)

Scopus Indexed Publications:

9. **Radha, S.**,G.Josemin Bala, E. Grace Mary Kanaga, Nagabushanam, P. *, "Scheduling and Adaptive Listening Approaches in MAC for WSN Applications – A Survey", *Journal of High Speed Networks*, vol. 26, no. 4, pp. 325-338, 2020(ISSN: 1875-8940), **IOS press**.(Web of Science Journal- without Impact Factor)
- 10.**Radha, S.** *, Shylu, D.S., Nagabushanam, P., Mathew, J., "Low noise amplifier with resistive and capacitive feedback for 2.4 GHz RF receiver front end", *Journal of High Speed Networks*, 25(2), pp. 181-203, Jun 2019.(ISSN: 1875-8940), **IOS press**. (Web of Science Journal- without IF)
- 11.Shylu Sam, D.S., **Radha, S.**, Jackuline Moni, D., Sam Paul, P., Jecintha, J., "Design of 1-V, 12-bit low power incremental delta sigma ADC for CMOS image sensor applications", *International Journal of Recent Technology and Engineering*, 7(5), pp. 249-254, Feb 2019.
- 12.**S. RADHA**, D.S. SHYLU, P. NAGABUSHANAM, J. SUNITHA KUMARI, "Design of RF LNA with resistive feedback and gain peaking for multi-standard application", *International Journal of Recent Technology and Engineering*, pp 100-107 Volume 7 Issue 4, Nov 2018. (**Blue Eyes**)

Non-Scopus Publications/ UGC Listed Journals:

- 13.Nagabushanam, P., George, S. T., Subathra M S P., &**Radha, S.*** (2020) 1D CNN Architectures for EEG Classification with Motor Imagery input of eyes open and eyes closed conditions. *Int. J. of Intelligence and Sustainable Computing*. (**IndersciencePublisher**).DOI: 10.1504/IJISC.2020.10032562
- 14.Nagabushanam, P., George, S. T., S. Raveena Judie Dolly, D., **Radha, S.***, (2020). Convolution Neural Network Architectures for Motor Imagery EEG Signal Classification. *International Journal of Artificial Intelligence and Machine Learning*, Volume 11, Issue 1, 15-22, 2021, Jan (**IGI Global Publisher**). DOI: 10.4018/IJAIML.2021010102
- 15.**S.Radha**, P. Nagabushanam, "Survey on Networking architectures and protocols for green smart environment" Published in *International Journal of Advanced Computer Technology (IJACT)*, ISSN: 2319-7900 Vol 3 Issue1 on 25 Feb 2014.

International / National Conferences:

1. Teja, C. A., **Radha, S.**, du John, H. V., & Nagabushanam, P. (2022, April). Analysis of Power in SRAM Cell with Various Pull up, Pull down and Pass Gate Transistors. In 2022 **IEEE** 11th International Conference on Communication Systems and Network Technologies (CSNT) (pp. 141-145). IEEE.
2. Tejaswini, C., Nagabushanam, P., Rajasegaran, P., Johnson, P. R., & **Radha, S.** (2022, April). CNN Architecture for Lung Cancer Detection. In 2022 **IEEE** 11th International Conference on Communication Systems and Network Technologies (CSNT) (pp. 346-350). IEEE.
3. Anudeep, K. B., Jagannath, D. J., **Radha, S.**, & Nagabushanam, P. (2022, April). Random Pattern Generation and Redundancy Analysis in Memories. In 2022 **IEEE** 11th International Conference on Communication Systems and Network Technologies (CSNT) (pp. 341-345). IEEE.
4. Reddy, G. S., **Radha, S.**, Taufiq, K. T., Reddy, K. D. S., Reddy, K. P. K., & Nagabushanam, P. (2022, January). Security based Electronic Voting Machine using Xilinx tool. In 2022 2nd International Conference on Power Electronics & IoT Applications in Renewable Energy and its Control (PARC) (pp. 1-4). **IEEE**.
5. Pradeep, A., **Radha, S.**, Sujay, N., Ram, P. V. J., Ganesh, G. V., & Nagabushanam, P. (2022, January). 64-bit Multi-match Priority Encoder (MPE64) for High Performance VLSI Architectures. In 2022 2nd International Conference on Power Electronics & IoT Applications in Renewable Energy and its Control (PARC) (pp. 1-5). **IEEE**.
6. J Joe Abinas, Hari Venkata Krishna Chandolu, P. Nagabushanam, **S. Radha**, Vempalli Murali Krishna, "Analysis of Diabetes Patients using Classification algorithms", presented in 2021 10th **IEEE** International Conference on Communication Systems and Network Technologies (CSNT), pp. 810–814, 18th to 19th Jun 2021 in Bhopal, India.
7. **S. Radha**, Basti Rishitha Shalini, Suram Anagalakshmi, Vundela Rajitha Reddy, P. Nagabushanam, "SYNCHRONOUS DUTY CYCLE IN MAC PROTOCOL FOR WIRELESS APPLICATIONS", presented in

- 3rd International Conference on Signal Processing and Communication (ICPSC), pp. 330–334, 9451758, 13th to 14th May 2021 in Karunya University, CBE.
8. P. Nagabushanam, Neema C Jayan, C. Antony Joel, **S. Radha**, “CNN Architecture for Diabetes Classification”, presented in 3rd International Conference on Signal Processing and Communication (ICPSC), pp. 166–170, 9451724, 13th to 14th May 2021 in Karunya University, CBE.
 9. P. Nagabushanam, S. Thomas George, Praharsa Davu, P. Bincy, Meghana Naidu, **S. Radha***, “Artifact Removal using Elliptic Filter and Classification using 1D-CNN for EEG signals”, presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2020), pp. 551–556, 9074287, organized by Sri Eshwar college of Engineering, CBE on 6th & 7th Mar 2020.
 10. **S. Radha**, G. Josemin Bala, S. Priya Lakshmi Khanna, J. Reshma, PMP. Chrisstoj, P. Nagabushanam*, “Contention window adjustment in MAC Protocol for WSN Applications”, presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2020), pp. 629–634, 9074371, organized by Sri Eshwar college of Engineering, CBE on 6th & 7th Mar 2020.
 11. **S. Radha**, Gadde Krishna, Kurra Megha Sai Sumanth, Yangareddy Sasidhar Reddy, Kondala Varun Kumar Reddy, P. Nagabushanam*, “Double Balanced Mixer with noise reduction for RF Receiver using Cadence 180nm Technology”, presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2020), pp. 665–670, 9074253, organized by Sri Eshwar college of Engineering, CBE on 6th & 7th Mar 2020.
 12. **S. Radha**, G. Josemin Bala, Stephy. V, Dominic Mary Princy, Divya. J, Sherene Johny Rithinika. P, “POWER OPTIMIZATION IN MAC PROTOCOLS FOR WSN”, 2nd IEEE International Conference on Signal Processing and Communication (ICSPC'19), pp. 168–172, 8976600, 29th to 30th MARCH 2019 in Karunya University, CBE.
 13. **S. Radha**, D.S. Shylu, Sanchana Priya M.S., Christobel Dianna, Aishwarya T, Hepziba S Selvamani, “Transformer Coupled Voltage Controlled Oscillator Using 180nm Technology in Cadence Tool”, 2nd **IEEE** International Conference on Signal Processing and Communication (ICSPC'19), pp. 173–177, 8976679, 29th to 30th MARCH 2019 in Karunya University, CBE.
 14. D.S. Shylu, **S. Radha**, P. Sam Paul, Parakati Sarah Sudeepa, “Design of low power 4-bit Flash ADC in 90nm CMOS Process”, 2nd **IEEE** International Conference on Signal Processing and Communication (ICSPC'19), pp. 252–257, 8976538, 29th to 30th MARCH 2019 in Karunya University, CBE.
 15. **S. Radha**, Wilson Vineetha, Darshana. S, G. Josemin Bala, P. Nagabushanam, “ScP Protocol for Wireless Sensor Networks” presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2019), pp. 1135–1138, 8728341, organized by Sri Eshwar college of Engineering, CBE on 15th & 16th Mar 2019.
 16. P. Nagabushanam, Praharsa Davu, S. Thomas George, **S. Radha**, “Morphological operations on EEG Signals for Spike Detection” presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2019), pp. 143–146, 8728407, organized by Sri Eshwar college of Engineering, CBE on 15th & 16th Mar 2019.
 17. **S. Radha**, G. Josemin Bala, P. Nagabushanam “Multilayer MAC with Adaptive listening for WSN” presented in **IEEE Sponsored** - International 3rd International Conference on Inventive Systems and Control (ICISC 2019), pp. 15–21, 9036423, organized by JCT college of Engineering, CBE on 10th & 11th Jan 2019.
 18. **S. Radha**, Nancy.R, Aiswarya Rajendran, Kunkanuru Supriya “Multi-Layer MAC with Adaptive Listening for Wireless Sensor Network” presented in **IEEE Sponsored** - International 3rd International Conference on Engineering and Technology (ICETECH 2018) organized by Rathinam Technical Campus, CBE on 30th & 31st August 2018.
 19. **S. Radha**, P. Nagabushanam, RB Hari Krishna, T. Samuel Franklin “Transformed Coupled Injection Based Differential VCO using 180nm Technology in Cadence Tool” presented in **IEEE Sponsored** - International 3rd International Conference on Engineering and Technology (ICETECH 2018) organized by Rathinam Technical Campus, CBE on 30th & 31st August 2018.
 20. **S. Radha**, P. Nagabushanam, K. Nikhil, Bitty Jose, Litty Jose, “High Speed Op-Amp for Video Applications” presented in **IEEE Sponsored** - International Conference on Intelligent Computing and Control Systems (ICICCS 2018), pp. 494–498, 8663036, organized by Vaigai College of Engineering, Madurai on 14th & 15th Jun 2018.
 21. P. Nagabushanam, **S. Radha**, Sushmita Selvadass, Kezia Kanishka Joseph, “Gabor filter based Image segmentation for Disease Detection using VHDL” presented in 2nd International Conference on Inventive Communication and Computational Technologies (ICICCT 2018), pp. 1807–1812, 8473083, organized by Rangannath Engineering College, Coimbatore on 20th & 21st Apr 2018.

22. **S.Radha**, P.Nagabushanam, Thanam Jaideep Reddy, B.Sachin, "Transmission Distance in MAC protocol for Wireless Sensor Networks" presented in 2nd International Conference on Inventive Communication and Computational Technologies (ICICCT 2018), pp. 1813–1818, 8473115, organized by Rangannath Engineering College, Coimbatore on 20th & 21st Apr 2018.
23. **S.Radha**, RB Hari Krishna, N.Paramasiva Pandi, Sneha Varghese, P.Nagabushanam, "Floor Planning of 16 bit Counter design for health care applications Using 180nm Technology in Cadence Tool" presented in 2nd International conference on Electronics, Communication and Aerospace Technology (ICECA 2018), pp. 484–487, 8474753, organized by RVS Technical Campus, Coimbatore on 29th & 30th Mar 2018.
24. **S.Radha**, G.Josemin Bala, "Multichannel MAC for Wireless Sensor Network applications" presented in 3rd International Conference on Communication and Electronics Systems (ICCES), pp. 1219–1222, 8724079, organized by RVS Technical Campus, Coimbatore on 15-16 Oct. 2018.
25. **S.Radha**, G.Josemin Bala, "Flooding in Static and Free Deployment for Underwater Wireless Sensor Networks" **IEEE** International Conference on Signal Processing and Communication (ICSPC'17), pp. 28–32, on 28th and 29th Jul 2017 in Karunya University, CBE.
26. P.Nagabushanam, S.Thomas George, **S.RADHA**, "Brain Disorders by Analyzing EEG Signals – A Review" presented in 2017 **IOSRD - International Conference** - ICDSME 2017 organized by International Organization of Scientific Research and Development, Coimbatore on 14th & 15th Apr 2017.
27. Junias M, **S. Radha**, "Power Aware Dynamic Ram Using Clock Gating Techniques" National Conference on Innovations in Future Communication Technologies - March 30-31 2017.
28. Lavanya A, **S. Radha**, "Power delay product optimization in Wallace tree multiplier" National Conference on Innovations in Future Communication Technologies - March 30-31 2017.
29. Rohit.S Aashish Nelson, **S.Radha**, "Implementation Of Flooding Based Routing In Static And Mobile Nodes For Underwater Wireless Communications" National Conference on Innovations in Future Communication Technologies - March 30-31 2017.
30. Reshma Shibi, Ashlin George Mathew, **S. Radha**, "Energy Efficient Routing in Underwater Wireless Sensor Network by Static Node Deployment" presented in 4th National Conference on Computational and Inter-Networking Information Technology - **NCIIT'17**, organized by Anna University, Coimbatore on 30th & 31st March 2017.
31. Vinita Sara Jacob, Daniya Dthomas, **S. Radha**, "Medium Access Control Protocol for Underwater Wireless Sensor Networks" presented in 4th National Conference on Computational and Inter-Networking Information Technology - **NCIIT'17**, organized by Anna University, Coimbatore on 30th & 31st March 2017.
32. P. M. Prem Thomas, R. Rishanth, **S.Radha**, "Implementation of Flooding in Protocols for Underwater Wireless Sensor Network" presented in 9th National Conference(**IEEE Sponsored**) on Signal Processing, Communication and VLSI Design (NCSCV' 17) , organized by Anna University, Coimbatore on 24th & 25th March 2017.
33. **S.Radha**, Raechel Alexander, Jeenu John, "Energy Model of Reactive Protocols for Underwater Wireless Sensor Networks" presented in 9th National Conference(**IEEE Sponsored**) on Signal Processing, Communication and VLSI Design (NCSCV' 17) , organized by Anna University, Coimbatore on 24th & 25th March 2017.
34. **S.Radha**, Nova Ann Jacob, Ann Shettilda S, "PACKET SIZE OPTIMIZATION IN PROTOCOLS FOR UNDERWATER WIRELESS SENSOR NETWORK" presented in 9th National Conference (**IEEE Sponsored**) on Signal Processing, Communication and VLSI Design (NCSCV' 17) , organized by Anna University, Coimbatore on 24th & 25th March 2017.
35. **S.RADHA**, Jisha Mathew, "Linearization of Low Noise Amplifier for Wireless Sensor Networks" presented in 2017 **IEEE** sponsored **International Conference** - ICISC 2017, 8068678, organized by JCT College of Engineering and Technology, Coimbatore on 19th & 20th Jan 2017.
36. Madona Sahaai, **S.RADHA**, "High Speed Energy Efficient Cell based Adder for VLSI Design" presented in 2016 **IEEE** sponsored **International Conference** - ICETECH 2016 organized by Rathinam Technical Campus, Coimbatore on 17th & 18th Mar 2016.
37. **S.RADHA**, G. Josemin Bala, "Reactive Protocols for Underwater Wireless Sensor Networks" presented in National Conference **NCACM'16** organized by Anna University, Regional Center, Coimbatore on 15th Mar 2016.
38. K. Siva Karthikeyan, **S.RADHA**, "Design of Energy Efficient DMM-MAC Routing Protocol for Underwater Sensor Networks" Presented in **International Conference ICE²-RTRI 2015** Organized by Electrical Sciences Dept, Sri Krishna College of Technology, and Coimbatore on 2nd, 3rd January 2015.

39. **S.RADHA**, G. Josemin Bala, "Survey on Underwater Acoustic Wireless Communication" Presented in National Conference **NCSCV'14** Organized by ECE Dept, Anna University Regional Center, Coimbatore on 9,10th May 2014.
40. **S.RADHA**, "Implementation of Vedic Multiplier with Reduced Power Consumption" Presented in National Conference **NEPECC'14** Organized by EEE Dept, Karunya University, Coimbatore, TN on 3rd & 4th Apr 2014.
41. **S.RADHA**, "Parallel Add-Compare-Select Unit Structure for high throughput trellis Decoder" Presented in **International conference ICSIE** Organized by Jawahar Engineering College, Chennai, TN on 6th Apr 2013.
42. **S.RADHA**, "Improved Pipeline for Add-Compare-Select Unit Structure of Viterbi Decoder" Presented in **International conference ICAET** Organized by Dr.MGR Educational and Research Institute University, Chennai, TN on 5th & 6th Apr 2013.
43. **S.RADHA**, "Power Efficient Low Latency Architecture for Decoder:Breaking The ACS-Bottleneck" Presented in **International conference ICIIOS** Organized by E&I Dept, Karunya University, Coimbatore, TN on 1st & 2nd Mar 2013.
44. **S.RADHA**, "A High Speed Low Power Pipelined Decoder –Breaking Through ACS Bottleneck" (2010 IEEE Paper Implemented- **my M.Tech project**) Presented in National Conference **NCECS** Organized by Vanjari Seethaiah College, Patancheru, Hyd on 12-13 Oct 2011

Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (**Attended and/or Organized**):

Organized- 19

1. Career Guidance (GRE, TOEFL, IELTS) by experts from Career Zone, Coimbatore - 21st Jul 2022.
2. Seminar on "Career Guidance" by experts from GATE Forum, Coimbatore on 22nd Apr 2022.
3. Training on "An effective project report preparation" 6th - 7th Apr 2022.
4. Orientation program on "GATE (Graduate Aptitude Test for Engineering) Exam" on 8th Mar 2022.
5. **National Science Day 2022** - Online Quiz Competition on 28th Feb 2022.
6. **2 days FDP on "Digital and analog design flow in cadence"** during 2nd – 3rd Dec 2021.
7. Awareness program - Opportunities After Engineering Through GATE & ESE on 29.09.2021
8. Opportunities after Engineering through GATE & ESE on 18th Mar 2021.
9. **Career Guidance – Lecture Series** on 23 and 30 May 2020.
10. One day workshop on Data Analysis using R Software 11th Mar 2019.
11. Orientation Program for GATE EXAM on 7th Mar 2019.
12. National Workshop on Device Modeling using Synopsys Sentaurus TCAD – 12th & 13th Nov 2018.
13. One day workshop on Implementation of Digital Logic Design in FPGA Kit on 18.9.2018
14. Orientation Program for GATE EXAM on 12th Jul 2018.
15. One day workshop on Data Analysis using R Software 15th Mar 2018.
16. Two days workshop on VLSI chip design & verification using Mentor Graphics EDA tool 1st – 2nd Mar 2018.
17. 5 Days Short Term Certified Course on Fundamentals of Digital Design & Verification (Bridging concepts to practice) using **CADENCE TOOL FLOW** during 29th Jan 2018 -2nd Feb 2018.
18. 5 Days Short Term Certified Course on Analog IC Design-Circuit and Layout Design Methodologies using **CADENCE ANALOG DESIGN FLOW** during 11th -15th December 2017.
19. National Workshop on ANALOG AND MIXED SIGNAL DESIGN USING CADENCE TOOL during 7th & 8th Apr 2016.

Attended: 69