

Name of Faculty D.Sony
 Designation Assistant Professor
 Nature of Job/Appointment Contract
 Date of Joining 3rd July 2019
 E-mail sonyd_ece@cbit.ac.in



| Education Qualifications | Name of the Degree | Class |
|--------------------------|-----------------------------|-------------|
| Ph.D. | | Pursuing |
| PG | M.Tech (VLSI System Design) | Distinction |
| UG | B.Tech (ECE) | Distinction |

Work Experience

Teaching 13 Years
 Research --
 Industry --
 Others --

Area of Specialization

Digital Design, Embedded Systems, GNSS

Academic Identity

Scopus Id: 57209745707
 Researcher Id: ABE-3865-2020 Orcid Id: 0000-0002-4595-2844

Professional Memberships

IAENG membership (Member No: 322711)

Responsibilities held at Institution Level

- Coordination of UG/PG Best Projects selection and activities related to Institute funded students projects.
- In charge for Student Attendance registers for UG and PG Programs
- Member, Course Expert Group
- Member, Common Course Committee

Research Guidance

Awards Received

Received Best Paper award in 2021

Courses Handled at Under Graduate / Post Graduate Level.

Digital Design, Microprocessors & Microcontrollers, Embedded System Design using RTOS, Linear Integrated Circuit and Applications, Signals and Systems. Electronic Devices and Circuits, Microprocessors & Microcontrollers Lab, Linear Integrated Circuit and Applications Lab, Electronic Devices and Circuits Lab.

No. of Papers Published

National Journals --
 International Journals -- 09
 National Conference -- 01
 International Conference -- 06

Projects Carried out

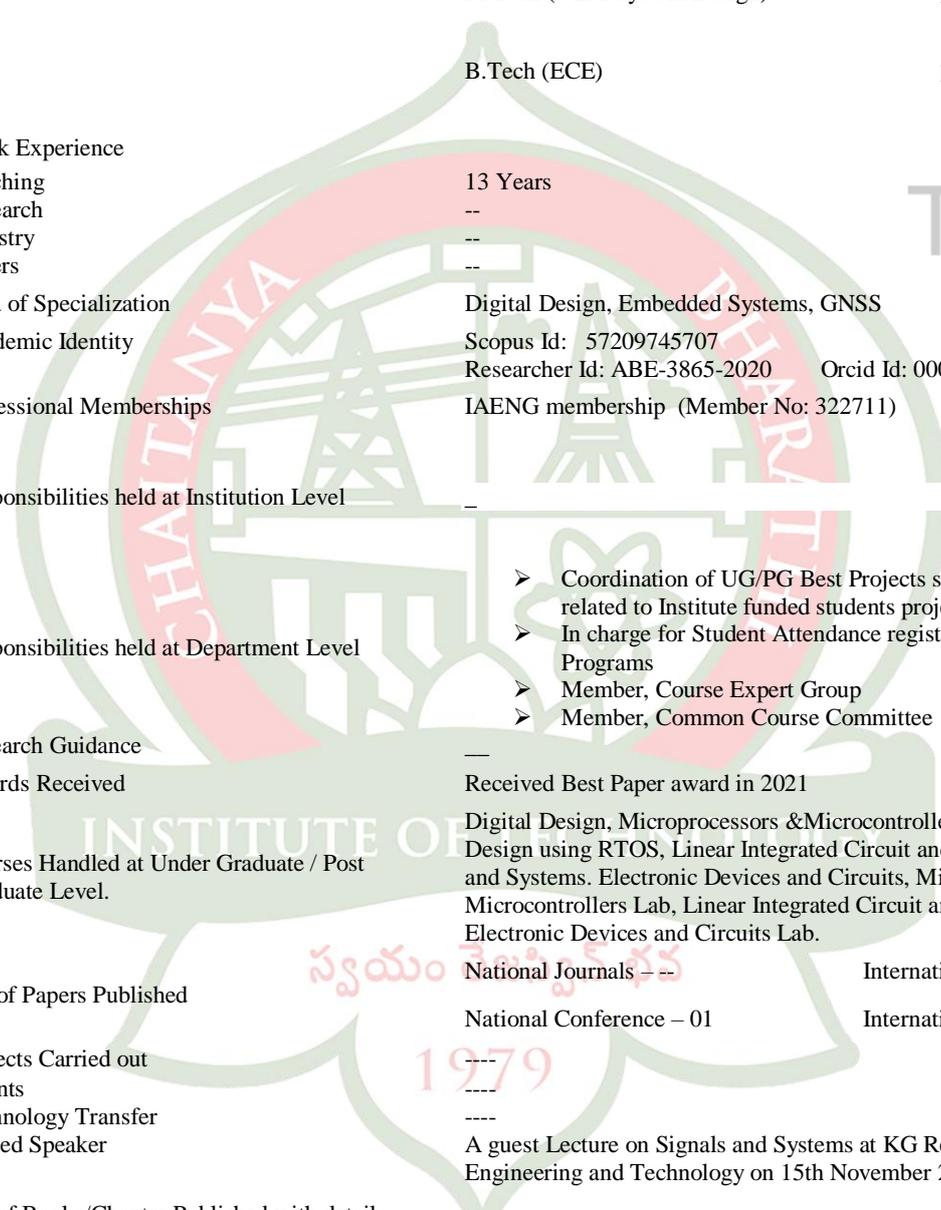
Patents

Technology Transfer

Invited Speaker

A guest Lecture on Signals and Systems at KG Reddy College of Engineering and Technology on 15th November 2021.

No. of Books/Chapter Published with details



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Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (Attended and/or Organized).

1. 3-Day Webinar on Signal Processing Techniques for Radar Applications, organized by “Institute Of Aeronautical Engineering”, on 10th, 12th and 14th July 2020.
2. One week National level online FDP on “Cyber Security”, organized by CBIT & MGIT in association with Supraja Technologies during 7th -12th July, 2020.
3. One week online FDP on “Future Nano Electronic Devices and Circuits”, organized by MGIT during 6th -10th July, 2020.
4. One week FDP on “Outcome Based Education and NBA Accreditation Process during 28th May- 1st Jun 2020.
5. Five Day STTP on “MATLAB based Teaching-Learning in Mathematics, Science & Engineering” organized by Ramrao Adik Institute of Technology, Nerul, Navi Mumbai in collaboration with DesignTech Systems Pvt. Ltd., Mumbai during 18th to 22nd May 2020.
6. A Two Day National Webinar on “Artificial Intelligence and Machine Learning” organized by Loyola Academy, Secunderabad during 20th -21st, May 2020.
7. A One Week Faculty Development Program on “Recent Trends in Advanced Communications” organized by Department of Electronics and Communication Engineering, Lakireddy Bali Reddy College of Engineering, Mylavaram, during 4th -9th, May 2020.
8. Completed NPTEL online Certification Course on “NBA Accreditation and Teaching –Learning in Engineering” during Jan-Apr 2020.
9. Attended One week refresher Course on “Recent trends in VLSI Design” by Department of ECE, NIT Meghalaya during 27th Sept -1st Oct 2021.
10. Attended “One week Asian regional workshop on SciTinyML: Scientific Use of Machine Learning on Low-Power Devices” organized by ICTP, Italy during 6th-10th June 2022.
11. Attended a 5-day online FDP on the theme “Inculcating Universal Human Values in Technical Education” organized by All India Council for Technical Education (AICTE) from 11th July to 15th July 2022.
12. Co-coordinator for A One week FDP on “Research Opportunities in Image Processing and Machine Learning”, conducted by the Department of Electronics and Communication Engineering, Chaitanya Bharathi Technology, Institute of during 29th June to 03rd July 2020.

Details of Journal Publications/ Conferences

International Journal publications

1. D.Sony, Krishna Reddy, P. Naveen Kumar, “Implementation of Receiver Autonomous Integrity Algorithm for Fault Detection of IRNSS”, “Aerospace Systems” Vol 5, issue 4.
2. D Sony ; N.Krishna Sandeep Reddy ; R V Krishna Sathvik ; Palle Vivekgoud(2021), “A Smart Parking System Using M2m Communication”, International Journal Of Electrical Engineering And Technology, Volume 12, Issue 5, May 2021, Pp. 109-115.
3. D. Sony, P. Keerthi, O. Aditya & N. Sravya, “Simulation and Performance Analysis of Interleave Division Multiple Access (IDMA) in Comparison with Code Division Multiple Access (CDMA)”, Asian Journal of Applied Science and Technology (AJAST), Volume 5, Issue 2, Pages 90-94, April-June, 2021.
4. M. Udaya, D. Sony, D. Krishna Reddy(2020), “Design of IRNSS Tracking System using 1.5 bit ADPLL and Correlator”, The International Journal of Innovative Technology and Exploring Engineering, Volume 09 issue 10, pp 413-420.
5. D.Sony, (2018), “Design of High speed fixed and reconfigurable FIR filter for speech signal processing”, International Journal of Advance Engineering and Research Development, Volume 05 issue 01, pp 136-148.
6. D.Sony, (2018), “Real-time Scene Text Detection via Connected Component Clustering and Non-text

- Filtering in videos”,International Journal of Advance Engineering and Research Development, Volume 05 issue 02, pp 801-806.
7. D.Sony, (2018), “Comparison of Wallace, Vedic and Dadda Multipliers”,International Journal of Advance Engineering and Research Development, Volume 05 issue 03, pp142-151.
 8. D.Sony, B.Ramu, (2018), “An adaptive image resolution enhancement by using multi resolution transform”, International Journal Of Creative Research Thoughts, Volume 06 issue 01, pp1235-1251.
 9. D.Sony, M.Krishna, (2018), “Motion Deblurr”,International Journal of Advance Engineering and Research Development, Volume 05 issue 03, pp 801-806.

International Conference Publications

1. D. Sony, D.Krishna Reddy, JB Farheen “Implementation of “Code Tracking Loop using Narrow Correlator for GNSS”, “IEEE 2022 Trends in Electrical, Electronics, Computer Engineering Conference (TECCON-2022)” (IEEE Conference Record# 54414) to be held on 26th & 27th May 2022, organized by School of EEE, in association with IEEE Student Branch REVA University.
2. D. Sony, D.Krishna Reddy, JB Farheen, P.Raj Reddy and S.Rajireddy,” Design of Code Tracking Loop using Narrow Correlator for GNSS”, Recent Trends in Engineering and Technology - 2021 (RTET - 2021) organized by the Department of Electrical and Electronics Engineering, KSRM College of Engineering (Autonomous) in virtual mode on December 29th, 2021.
3. D.Sony,S. Chadravardan,Dr.D. Krishna Reddy, Dr.P.Naveen Kumar,” Satellite Selection Algorithm for RAIM Applications”, “INTERNATIONAL CONFERENCE on EMERGING TRENDS IN CIRCUIT1 - BRANCH TECHNOLOGIES AND APPLICATIONS(2021).
4. D. Sony, JB Farheen, Dr. D. Krishna Reddy and Dr.P. Naveen Kumar, “Integrity Monitoring of GNSS using Ztest” , “INTERNATIONAL CONFERENCE on EMERGING TRENDS IN CIRCUIT1 -BRANCH TECHNOLOGIES AND APPLICATIONS(2021).
5. D.Sony, Dr.D.Krishna Reddy ,Dr.P.Naveen Kumar, “Statistical Characterization of IRNSS Satellite Clock Error for Receiver Autonomous Integrity”, Third International Conference Soft Computing and Signal Processing" (ICSCSP-2020). https://link.springer.com/chapter/10.1007/978-981-33-6912-2_58
6. D. Sony, V. Satya Srinivas, P. Naveen and D. Krishna Reddy, “IRNSS User Range Accuracy Evaluation for Receiver Autonomous Integrity”, URSI Asia-Pacific Radio Science Conference (AP-RASC) (2019).

National Conference Publications

1. D. Sony , D. Krishna Reddy, P. Naveen , and V. Satya Srinivas, “IRNSS and GPS Satellite User Range Accuracy Analysis for Receiver Autonomous Integrity Monitoring”, 2019 -ISRO National Space Science Symposium (NSSS 2019) , Savitri Bhai Phule Pune University, Pune, January 2019.

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