

Name of Faculty Dr. Chandra Sekhar Pedada
 Designation Assistant Professor
 Nature of Job/Appointment Regular
 Date of Joining 19 - 07 - 2012



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Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (ECE)	Awarded
PG	M. Tech. (Electronics)	First class
UG	B. Tech. (ECE)	First Class

Work Experience

Teaching	18 Years
Research	04 years
Industry	--
Others	--
Area of Specialization	5G Wireless communication
Professional Memberships	Fellow IETE -(F-502567) IAENG- (290579) IFERP- (PM-86039517) IEEE-(180711-000092)
Responsibilities held at Institution Level	--
Responsibilities held at Department Level	1. In-charge Teaching Learning Process 2. Member Course audit and department external audit committee 3. Member Course Expert Group. 4. Worked for C2 for NBA, NAAC
Research Guidance	8 ME projects has been guided.
Awards Received	--
Courses Handled at Under Graduate / Post Graduate Level.	Signal & Systems, Analog Circuits, VLSI Design, Analog IC Circuits, Digital IC Circuits, LDIC, Digital System Design,
No. of Papers Published	National Journals – -- International Journals – 8
Projects Carried out	National Conference – స్వయం సేవస్విన భవ International Conference – 04
Patents	--
Technology Transfer	1979 --
Invited Speaker	1. Text book entitled “5G and IoT” published by DIA Publications with ISBN No: 978-81-962206-2-4. 2. Text book entitled “Concepts of 5G Technology” Published by RK Publications with ISBN no: 978-81-961790-9-0. 3. Text book entitled “VLSI Design” published by GCS Publications with ISBN No:987-81-963125-2-7.
No. of Books Published with details	

Workshop Organized:

- Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops.
1. Department of Electronics and Communication Engineering R & D Centre In association with IEEE-Signal Processing Society – Students Branch Chapter Organized Webinar on “Mega Trends in Aerospace Industry” on 12th June 2021

Other Trainings (**Attended and/or Organized**).

2. Successfully Organized a One Week Faculty Development Program on **"IoT for Emerging Applications"** during **04th – 08th February, 2020** in association with Electronics & ICT Academy IIT Guwahati at CBIT.
3. Successfully Organized a Two-Days Workshop **"Analog VLSI Design – Bridging gap between classroom and industry"** in association with 'Joint chapter of CAS /EDS IEEE Hyderabad section on **17th and 18th of March, 2017**.
4. Successfully Organized a Two-Day Workshop on **"Architectures and Applications of FPGA & CPLD"**, **10th & 11th March-2016**.
5. Successfully Organized two day a National Level Student Technical Symposium – **SYNAPSE 2K15** on **29th & 30th September-2015**.
6. Conducted an AICTE sponsored Two Week- FDP on **"Recent Trends and Advancement in VLSI for Embedded Applications"** at CBIT during **15-27 June 2015**.
7. Organized a Three-Day Workshop on **"VLSI Design Using Cadence Tools"**, at CBIT during **27-29 March 2014**.

Workshop attended:

1. Completed course on **"AWS IoT core using Industry grade microcontroller"** training in **ORIGIN TECH** from 10 march 2023 to 10 April 2023.
2. Attended course on **"Basics of Research methodology"** on **August 26th 2022** (online) conducted jointly by **Kanpur Institute of Technology -India** and **APTİKOM SULUT-Indonesia**.
3. Completed successfully two week **AICTE Training And Learning (ATAL) Academy** Blended/Hybrid FDP on **"iATP(indigenous Automatic Train Protection) system - KAVACH"** from 2022-09-05-2022-09-10 to 2022-09-12-2022-09-16 at **Indian Railway Institute of Signal Engineering and Telecommunications**.
4. Successful completion of **One Week Online Faculty Development Programme** on **"Blockchain assisted Federated Machine Learning for Securing Big Data Analytics and IoT"** organized by **Department of Information Technology** from **08th -12th April, 2022** by **Dr B R Ambedkar National Institute of Technology, Jalandhar**.
5. Successfully completed a **12-days "PRICE ACTION TRADER PROGRAM-Technical analysis"** organized by **chartingspace** from **01-03-2022 to 18-03-2022**
6. Attended FDP on **"D3 GUI Based FPGA Programming"** from 20th to 24th December 2021 conducted by **ECE - SRMIST, Vadapalani** in collaboration with **VI Solutions**.
7. Successfully completed online **Entrepreneurship Development Program** on **"FOOD PROCESSING"** from 13.12.2021 to 17.12.2021 organized by **MSME-Technology Development Centre (PPDC)**.
8. Attended the five-day national level online FDP on **"A Systematic Navigation from Research To Research Proposals 2.0"** from **22nd – 26th November 2021**, organized by **Department of Information Technology in collaboration with Institutions Innovation Council, MVSr Engineering College**.
9. Attended the **"One Week Short Term Training Program (AICTE Sponsored) on Next Generation Wireless Technologies: 5G and beyond"** held from **20th September 2021 to 25th September 2021**, by **Indo Global College of Engineering, Abhipur, New Chandigarh**
10. Attended the **One Week Short Term Training Program** on **"Graphical System Design in Engineering & Research Applications"**, **Vidyavardhaka College of Engineering, Mysuru** in association with **VI Solutions, Bengaluru** from **6th to 11th September 2021**.

11. Attended national webinar on “**Antennas for 5G communications**” on **15th July, 2021** Vignan’s Institute of Engineering for Women
12. Attended webinar on “**Antenna design for 5G**” organized for Department of ECE, Vignan's Institute of Engineering for Women Visakhapatnam on **14th July 2021**
13. Completed course on “**Matlab course for wireless communication engineering**” on **13 July 2021** from Udemy online courses.
14. Attended AICTE sponsored One Week STTP “**Recent Trends & Applications of Wireless Communication in VLSI Design**” organised by Priyadarshini College of Engineering, Nagpur, 22nd to 27th March 2021.
15. Attended AICTE sponsored One Week STTP “**AI-MIMO MILLIMETER (mm) Wave and Massive MIMO Applications for 5G Wireless Network using AI**” organised by Gokaraju Rangaraju Institute of Engineering and technology, 25 January to 1February 2021
16. Attended AICTE Sponsored STTP, “**Hands On Project Based Approach of 5G Design and Development**” (Phase-III) organized by Department of ECE, KITS, Warangal18th - 23rd January 2021
17. Attended AICTE sponsored One Week STTP “**Massive MIMO and Millimeter Wave MIMO Technologies for 5G Networks**” (Phase-II) organized by Dept of ECE -Erode Sengunthar Engineering College (TN) 07.09.2020 to 12.09.2020
18. Attended One week FDP-JNTU-Kakinada “**Recent advancements in Communication and Signal Processing**” from 24-8-2020 to 29-8-2020
19. Attended One week **refresher course** organized by CBIT(A)- Geervana Bharathi CBIT(A), 29-6-2020 to 5-7-2020
20. Attended AICTE sponsored online STTP on “**Massive MIMO and Millimeter Wave MIMO Technologies for 5G Networks**” held from 07.09.2020 to 12.09.2020 (Phase-II).
21. Attended one-week online FDP on “**Recent advances in communication and signal processing**” from 24th to 29th August 2020 at JNTUKCOE Vizianagaram.
22. Participated in the webinar on “**Present Trends and Research scopes in 5G Wireless Communications**” conducted by the Department of Electronics and Communication Engineering, GPREC, on 27th and 28th June, 2020.
23. Participated in webinar on “**Block Level Design using IP Integrator in Xilinx Vivado**” held on 26th June 2020 in association with Core EL Technologies and Xilinx
24. Participated in the webinar on “**Publishing Research Articles in Scopus Indexed Journals**” conducted by CBIT School of Management Studies on 24th June, 2020.
25. Participated in a webinar on “**Research challenges and Career Opportunities in VLSI**” held on 6th June 2020 organized by department of ECEC, NBKR Institute of Science and technology-AP
26. Successfully Completed a one-week FDP on “**Outcome Based Education and NBA Accreditation Process**” at CBIT during 28-05-2020 to 01-06-2020.
27. Participated in webinar organized by Internal Quality Assurance Cell (IQAC), Rai University on “**How to publish research Articles in CI Journals using Info-Graphic Tools**” on 4th June 2020.
28. Successfully Completed “**TCP/IP and Advanced Topics**” a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 13-05-2020.
29. Successfully Completed “**Peer-to-Peer Protocols and Local Area Networks**” a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 13-05-2020.
30. Successfully Completed “**Packet Switching Networks and Algorithms**” a 5-Week online non-credit course authorized

- by University of Colorado and offered through Coursera on 13-05-2020.
31. Successfully Completed "" a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 13-05-2020.
 32. Successfully Completed "**Introduction to Electronics**" a 7-Week online non-credit course authorized by Georgia Institute of Technology and offered through Coursera on 17-05-2020.
 33. Successfully Completed "**AI for Everyone**" a 4-Week online non-credit course authorized by i and offered through Coursera on 11-05-2020.
 34. Successfully Completed "**Wireless Communications for Everybody**", a 6-Week online non-credit course authorized by Yonsei University and offered through Coursera on 11-05-2020.
 35. Successfully completed the "**Effective and Efficient Online Teaching in the Age of Corona, A Hands on Workshop**" on 17 May 2020 organised by the Teaching Learning Centre (ICT) at IIT Bombay, under the Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMMNMTT), MHRD, Govt. of India.
 36. A Five Days Online Faculty Development Programme on "**Recent Communication Tools and its Application**" Organized by Department of Electronics and Communication Engineering, St. Martin's Engineering College From 19th to 23 may 2020
 37. One week programme "**NAAC Awareness Programme for Faculty**" organized by Marathwada Mitra Mandal Institute of Technology (MMIT), Pune, online during the period 08/05/2020 to 14/05/2020.
 38. Attended a five days online webinar on "**Knowledge Sharing Session on Recent Trends and Its Application in Electronics**" Organized by Department of Electronics and Communication Engineering St. Martin's Engineering College from 03rd to 07th May, 2020
 39. Attended a 5-Day STTP on "**MATLAB based Teaching – Learning in Mathematics, Science and Engineering**" organized by the Department of Electronics Engineering, , Nerul, Navi Mumbai and B&R Automation Pvt. Ltd. Pune during 8-12 June2020.
 40. Participated in the webinar on "**Analog IC Design using Mentor EDA Tools**" held on 10th April 2020 in Association with CoreEL Technologies and Mentor
 41. Successfully completed a 12 Week NPTEL-(FDP) course "**NBA Accreditation and Teaching - Learning in Engineering (NATE)**", on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 semester.
 42. Successfully completed an 8 Week NPTEL-(FDP) course "**Fundamentals of MIMO Wireless Communication**", on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 semester.
 43. Successfully completed a 8 Week NPTEL-(FDP) course "**Evolution of Air Interface towards 5G**", on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 semester
 44. Successfully completed an 8 Week NPTEL-(FDP) course "**Hardware Modeling using Verilog**", on the SWAYAM platform (swayam.gov.in), during the Aug.-Nov. 2019 semester.
 45. Successfully completed a 8 Week NPTEL-(FDP) course "**CMOS Digital VLSI Design**" on the SWAYAM platform (swayam.gov.in), during the Feb-Apr 2019 semester.
 46. Attended "**88th Orientation Course**" at Osmania University held between 13th December 2018 to 11th January 2019, conducted by UGC-HRDC-Osmania University-Hyderabad.
 47. Attended AICTE sponsored two-week Faculty development programme on "**Simulation of Wireless sensor networks using NS-2**" during 20th-November to 2nd December 2017 by Sreenidhi Institute of Science and Technology.

48. A One Week course on “**Analog Integrated Circuit Design Using Cadence Analog Design Flow**” at Entuple Technologies, Bangalore during 03-07 Oct 2016.
49. Attended a one-day intense course on “**Labview**” on 20th November 2015 in the department of ECE, CBIT.
50. Attended two-week Faculty development programme on “**VLSI Design**” organized by Department of ECE at VNRVJIT during 20th May 1st June 2013.
51. Attended two-day Faculty development programme on “**Analog and Digital VLSI Design**” organized by Department of ECE at MVSR College of Engineering during 21st-22nd January 2013.
52. Attended three-day national level Faculty development programme on “**ECAD & VLSI Design**” organized by Department of ECE at MGIT during 6th to 8th September 2012. Xavier Engineering College, Tirunelveli on 10/07/2020.

Details of Journal Publications/
Conferences (National and
International)

International Conference:

1. **P. Chandra Sekhar**, T. S. N. Murthy, “Physical Layer Security using SSA” has been accepted for ISCAS-2023.
2. Krishan K.S.G, **P. Chandra Sekhar** etc “Machine Learning Algorithms for 5G and Internet-of-Thing (IoT) Networks” 2022 Second International Conference on Advances in Electrical, Computing, Communication and Sustainable Technologies (ICAECT) doi: 10.1109/ICAECT54875.2022.9808005
3. **P. Chandra Sekhar**, T. S. N. Murthy, “**Physical Layer Security using SMO**” 2022 IEEE-International Conference on Computing, Communication and Power Technology (IC3P), doi: 10.1109/IC3P52835.2022.00028

International Journal :

1. **P. Chandra Sekhar** ,E.Chandrasekhar "Machine Learning-Based Human Detection and Recording from IoT Surveillance System", Gradiva Review Journal Volume 9 issue 4 April 2023 DOI:10.37897/GRJ (SCPOUS)
2. Raj Kumar Patra, **P. Chandra Sekhar** etc., “user-segregation based channel estimation in the MIMO system”, Physical Communication <https://doi.org/10.1016/j.phycom.2022.101971> (SCI)
3. Vani S, **Chandra Sekhar P** etc., “**Detection of normal and epileptic EEG signals using by lifting based HAAR wavelet transform and artificial neural network**”, International Journal of Systems Assurance Engineering and Management, <https://doi.org/10.1007/s13198-021-01454-8>- November 2021(SCIE).
4. **P. Chandra Sekhar**, B.Raghuvier, “**LNA design for 5G applications**” Journal of Electronics and Communication Engineering and Technology (JECET) Volume 3, Issue 1, (**Impact factor 3.07**), July 2021 (Scopus Indexed).
5. E.Chandrasekhar, **P. Chandra Sekhar** “**Plant monitoring system using image processing**” IJRSET Volume 10 issue 6 June 2021|ISSN: **2319-8753**| **Impact factor 7.569**
6. **P. Chandra Sekhar**, Shravani Reddy Voddula. “Design of GaAs Based LNA at 26 GHz Band” IJCRT | Volume 9, Issue 6 June 2021 | ISSN: 2320-2882| (**Impact factor 7.79**)
7. Sarvade Venkata Manogna, **P. Chandra Sekhar**, “**Vertical-Horizontal Binary Common Sub-Expression Elimination based FIR Filter using XOR and MUX based Adder for DWT Applications**”, International Journal of Management, Technology and Engineering (UGC Care) (Volume X, Issue VII, JULY/2020) | (**Impact factor 6.5**)
8. N. Alivelu Manga, E.Chandrasekhar, **P. Chandra Sekhar**, “**Design and Simulation of 12-bit Current Steering DAC**”, International Research Journal of Engineering and Technology (IRJET) (UGC Care) (Volume: 06 | Issue: 07 | July 2019)| (**Impact factor 7.87**)