1	Name of Faculty	B. Neeraja	
2	Designation	Assistant Professor	
3	Nature of Job/Appointment	Regular	
4	Date of Joining	28-01-2011	
5	E-mail	bneeraja_ece@cbit.ac.in	

6	Education Qualifications	Name of the Degree	Class
	Ph.D	Ph.D.	Registered
	PG	M.E.(Digital Systems)	First with Distinction
	UG	B.TECH(ECE)	First with Distinction

7	Work Experience	
	Teaching	12 Years
	Research	5 years
	Industry	--
	Others	06 Months

8	Area of Specialization	Signal Processing, Wireless Communication and VLSI
9	Professional Memberships	Member of IETE, IAENG

10	Responsibilities held at Institution Level	1. Faculty coordinator of CBIT IETE Students Forum (ISF)-2023. 2. Member of AICTE Disciplinary Committee-2019. 3. Coordinator of student clubs Chaitanya Seva-(Street cause& Engineers without borders),Chaitanya Kreeda-sports , Chaitanya Sudhee-2019. 4. Member of Publicity committee for SHRUTHI from 2017 to 2019.
11	Responsibilities held at Department Level	1. In-charge ,Electronic Devices/Analog Circuits Lab from 2013. 2. In-charge , individual salary statements for all the faculty of the Department from 2016. 3. Subject Co-Ordinator, EECE (Equivalent Subject) from 2019. 4. Member, Disciplinary Committee of the Department from 2019. 5. Member, Course Expert Group-2019. 6. Member, Common Course Committee -2019. 7. In-charge, National Career Services -2019. 8. In-charge ,Attendance Statements. 9. In-charge , Sessional Marks . 10. Mentor , Student Progress report. 11. Worked for NAAK,NBA-UG and NBA-PG.
12	Research Guidance	Guided 09 M.E. projects
13	Awards Received	--

14	Courses Handled at Undergraduate / Post Graduate Level.	Electronic Devices and Circuits, Analog Circuits, Basic Electronics, Elements of Electronics and Communication Engineering, Wireless Mobile Communication, Radar and Satellite Communication, Coding Theory and Techniques
15	No. of Papers Published	National Journals – 00 International Journals – 02 National Conference – 01 International Conference – 04
16	Projects Carried out	**--**
17	Patents	**--**
18	Technology Transfer	**--**
19	Invited Speaker	1. Resource person for the five day intensive course on "Hands on LabVIEW" under LabVIEW academy school from 21/04/18 to 28/04/18. 2. Delivered a lecture in a four day intensive course on "Hands on Lab VIEW" under LabVIEW academy school from 27/11/17 to 30/11/17.
20	No. of Books/Chapter Published with details	--
21	Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (**Attended and/or Organized**).	1. Participated and successfully completed the 5-day online FDP on the theme "Inculcating Universal Human Values in Technical Education" organized by All India Council for Technical Education (AICTE) from 6th June, 2022 to 10th June, 2022. 2. Participated in the One Week Short Term Training Programme on "Cyber Security" jointly organized by Department of Computer Science & Engineering and Department of Information Technology, Chaitanya Bharathi Institute of Technology (Autonomous) during 16-20 May 2022. 3. Participated in the One Week online Faculty Development Program on "Advancement in Wireless Technology, Networking and Applications" organized by Department of Electronics and Communication Engineering CVR college of Engineering, Hyderabad during 21-25 March 2022. 4. Attended one week AICTE Sponsored Online Faculty Development Programme on "Advances in Optoelectronics, Devices and Communication" from 17th January, 2022 to 22th January, 2022 organized by Department of Electronics and Communication Engineering, Guru Jambheshwar University of Science & Technology, Hisar, Haryana-125001. 5. Attended the 3 day National Workshop on" Recent Trends in Radar Signal Processing" organized by the Department of Electronics and Communication Engineering and technically sponsored by I E T E, New Delhi from 3 rd 5 th January, 2022. 6. Participated and successfully completed AICTE Training And Learning (ATAL) Academy Online Elementary FDP on "Design Thinking" from 2021-06-21 to 2021-06-25 at Bharati Vidyapeeth College of Engineering. 7. successfully completed "ONE WEEK STTP ON EMERGING TRENDS IN VLSI TECHNOLOGY(SLOT-4)" organized by Malla Reddy Engineering College for Women during 14th to 19th June, 2021. 8. Participated in the 3-Day Webinar on "Signal Processing Techniques for Radar Applications" on 10 th , 12 th and 14 th July 2020 organized by IEEE-IARE Student Branch SPS Chapter.

9. Participated in the IIC Online Sessions conducted by Institution's Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 nationwide lockdown.

10. Successfully Completed "MATLAB Onramp" online course authorized by Mathworks Training Services on 19/05/2020.

11. Successfully Completed "Introduction to Electronics "an online non-credit course authorized by Georgia Institute of Technology and offered through Coursera on 14/05/2020.

12. Successfully Completed "AI for Everyone "an online non-credit course authorized by Stanford University and offered through Coursera on 02/05/2020.

13. Successfully Completed "LabVIEW Core 2" online course authorized by NI Customer Education on 27/04/2020.

14. Successfully Completed "LabVIEW Core 1" online course authorized by NI Customer Education on 21/04/2020.

15. Successfully Completed "Wireless Communications for Everybody "an online non-credit course authorized by the University of Yonsie and offered through Coursera on 06/04/2020.

16. A One Week online Faculty Development Program on "LaTeX & Technical Report", 25 - 30 May, 2020 organized by the Department of Mathematics, Kakatiya University, Warangal, Telangana, in association with Spoken Tutorial, IIT Bombay.

17. Successfully Completed a One Week Faculty Development Program on "Outcome based education and NBA accreditation process" organized by CBIT, Hyderabad, during 28/05/20 to 01/06/20.

18. A One Week online Faculty Development Program on "Recent trends in Advanced Communication", Lakireddy Bali Reddy College Of Engineering, Mylavaram, Krishna(Dt) from 04th -9th May 2020.

19. Successfully Completed NPTEL-AICTE course on" Principles and Techniques of Modern Radar Systems", during Jul-oct, 2019.

20. Participated in one day workshop on "Significance of MATLAB in Applications of Emerging Technologies" Department of Electrical and Electronics Engineering, CBIT, Hyderabad on 26th June 2019.

21. A One Week FDP on "5G Wireless Communication" organized by Electronics and ICT Academy, NIT Warangal. Meity, Govt. of INDIA, during29th May to 3rd June,2019.

22. Attended 1 week short term training programme through ICT mode on "Development of Laboratory Instruction and Manual "organized by CBIT, Hyderabad during 4 feb to 9 February, 2019.

23. Qualified and Certified LabVIEW Associate developer (CLAD) exam and recognized as one of the member in "NI LabVIEW" academy from Aug 2017 to Aug 2019.

24. Organized a Two week workshop on "Hands on Lab VIEW" under Lab VIEW academy school during 18/06/18 to 30/06/18.

25. Attended a two week refresher course on "Spectral

26. Methods for Engineering & Scientific computing Applications" at IIT Bhubaneswar ,Govt.of India, MHRD on 5 th – 16 th June 2017.

27. Attended a workshop on MATLAB Expo 2017 and gained knowledge in HDL code writing.

22	Details of Journal Publications/ Conferences (**National and International**)

International Journal

1. B. Neeraja, N .V. Koteswara Rao , B. Rajendra Naik"Detection and Analysis of closely spaced Multiple Targets using Modified MUSIC method" Journal of Advanced Research in Dynamical and Control Systems ,Volume 12, Issue 7, 2020, Pages 2857-2865, Doi: 10.5373/JARDCS/V12SP7/20202427.

2. Neeraja B, Ramya K "Design and Analysis of an Energy Efficient 4-bit Barrel Shifter Circuits in 45nm Technology", International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 – 8958, Volume-9 Issue-4, April, 2020.

International /National Conferences

1. B. Neeraja, N .V. Koteswara Rao , B. Rajendra Naik and K. Harshitha, "Design of High Speed Ethernet Based Receiver Processor Unit for Radar applications," 2022 4th International Conference on Advances in Computing, Communication Control and Networking (ICAC3N), Greater Noida, India, 2022, pp. 1656-1660, doi: 10.1109/ICAC3N56670.2022.10074480.

2. Neeraja Bandi, R. Ramavath and S. K. Meda, "Design and Analysis of PTL based Basic gates and Adder with Improved Performance," 2020 International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT), Bangalore, India, 2020, pp. 184-188, doi: 10.1109/RTEICT49044.2020.9315581.

3. Neeraja Bandi, Sathish kumar meda," Multiply-Accumulate unit for Binary Arithmetic using high speed adders and PIPO Accumulator", Scopus indexed Second International Conference on Computing, Communication and Energy Systems 2020 (ICCCES 2020), MEA College of Engineering, Perinthalmanna, Kerala, India, 2020, pp. 1-5.

4. B. Neeraja and R. S. P. Goud, "Design of an Area Efficient Braun Multiplier using High Speed Parallel Prefix Adder in Cadence," 2019 IEEE International Conference on Electrical, Computer and Communication Technologies (ICECCT), Coimbatore, India, 2019, pp. 1-5, doi: 10.1109/ICECCT.2019.8869307.

5. T. V. Vardhan, B. Neeraja, B. P. Kumar and C. S. Paidimarry, "Implementation of turbo codes using verilog-HDL and estimation of its error correction capability," 2015 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics (PrimeAsia), Hyderabad, 2015, pp. 75-79, doi: 10.1109/PrimeAsia.2015.7450473.