

Name of Faculty A. KRISHNA KUMAR  
 Designation Assistant Professor  
 Nature of Job/Appointment Regular  
 Date of Joining 26-12-2005  
 E-mail krishnakumar\_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (VLSI/Image processing)	Pursuing
PG	M. Tech (VLSI Design)	First
UG	B. E.(ECE)	First

#### Work Experience

Teaching	18 Years
Research	--
Industry	01
Others	--

Area of Specialization Digital Design, VLSI Design, FPGA Architectures

Professional Memberships Fellow IETE (F-502558), Member International Association of Engineers

Responsibilities held at Institution Level In-Charge, Intercom

Responsibilities held at Department Level

1. In-charge for ISO
2. Member, Course Expert Group
3. In-charge-Campus Radio
4. In-charge-SP/EDA Lab
5. In charge Lab Procurement-ECE
6. In charge HAM Radio

Research Guidance --

Awards Received --

Courses Handled at Under Graduate / Post Graduate Level. VLSI Design, Verilog HDL, CPLD & FPGA Architectures, Electronic Devices and Circuits, Basic Electronics, Digital Integrated Circuits and Applications, Advanced VLSI Design, VLSI Technology, Pulse and Digital Circuits, Advanced Digital Design through VHDL.

No. of Papers Published National Journals – 00 International Journals – 08

National Conference – 03 International Conference – 01

Projects Carried out --

Patents --

Technology Transfer --

Invited Speaker --

No. of Books/Chapter Published with details

1. V.Ramakrishna, Krishna Kumar Alenoor, Nanoscale Semiconductors, 1, Fundamentals Of TFET And Their Applications, 21 To 42, CRC Press, 2.

2. Participated a One-week Online FDP on “Research Opportunities in Image Processing and Machine Learning”, conducted by the Department of ECE, CBIT, during 29th to 03rd July 2020.
3. Passed the course “NBA Accreditation and Teaching – Learning in Engineering (NATE) – NPTEL Online Certificate.
4. Successfully attended Leadership Talk with Shri Dipendra Manocha, (Motivational Speaker), by MHRD’s Innovation Cell, held on 27th June 2020.
5. Attended One-week online FDP on “Recent Trends in VLSI Design” organized by the Department of ECE, Lakshmi Narain College of Technology & Science, Bhopal MP, from 22nd to 26th June 2020.
6. Participated in webinar on “Publishing Research Articles in Scopus Indexed Journals” conducted by CBIT – School of trends in VLSI”, conducted by Department of ECE, GRIET during 2nd to 7th June 2020.
7. Successfully participated in Webinar organized by Internal Quality Assurance Cell (IQAC), Rai University, Ahmedabad, Gujarat, on “How to publish Research Article in SCI Journals using Info-Graphic Tools” held on 4th June 2020.
8. Attended One-week online FDP on “opportunities & Challenges in Electronics & Allied Industries in India post COVID-19” organized by Department of Electronics Engineering in association with Institution’s Innovation Council (IIC) of Vivekanand Education Society’s Institute of Technology from 25th to 30th May 2020.
9. Successfully completed “VLSI CAD Part I : Logic ” an online non-credit course authorized by Urbana-Champaign and offered through Coursera, May 2020.
10. Successfully completed “AI for Everyone” an online non-credit course authorized by deeplearning.ai and offered through Coursera, May 2020.
11. Participated and Completed 5 Days Online FDP on “R Programming” organized by Indo Global Group of Colleges, New Chandigarh in association with IIT Bombay Spoken Tutorial ( An Initiative of National Mission on Education through ICT, MHRD, Govt. of India) from 27th April to 1st May 2020.
12. Successfully completed One-week National Faculty Development Program and Online Training on “LaTeX” Organised by Sanjay Ghodawat University, Kolhapur in association with Spoken Tutorial Project, IIT-Bombay during 27th April to 2nd May 2020.
13. Attended a Three-day online workshop on “Education 4.0” organized by IQAC, Atharva College of Engineering held on 28th-30th April 2020.
14. Participated and Successfully completed AICTE Training and Learning (ATAL) Academy Online FDP on “Internet of Things (IoT)” at National Institute of Technical Teachers Training and Research, Chandigarh from 20th to 24th April 2020.
15. Successfully completed “VLSI Digital Design using Verilog and hardware: Handson\_temp” online course on 15th April 2020.
16. Participated in Mentor Webinar on “Analog IC Design using Mentor EDA Tools” in association with CoreEL Technologies and Mentor held on 10th April 2020.
17. Successfully completed online course “Foundations to Open Education and OERs repositories” MOOC Course UNED Abierta (20-04-2020 to 30-06-2020) on 6th April 2020.
18. Successfully completed one-week FDP on “IoT for emerging Applications” organized by E&ICT academy, IIT Guwahati in association with Department of ECE, CBIT, held from 04-08 February 2020.
19. Participated in one day Symposium on Significance of Sanskrit for Technological Research, in association with Samskrita Bharathi, Telangana organized by Geervana Bharathi of CBIT on 8th September 2018.

Details of Journal Publications/  
Conferences (National and  
International)

**International/National Journal from the year 2017**

1. Krishna Kumar, G. Ashritha(2020) "Chat Application via UDP Protocol in C" , International Journal for Research in Applied Science & Engineering Technology (IJRASET), Volume 8 Issue V,pp 1219-1324.
2. A. Krishna Kumar, D.Deepika, V. Ramakrishna, Design Of Smart Fertilizer Chain System From Factory To Farmer, 19th International Conference on Electrical Engineering, Computing Science and Automatic Control (CCE) Mexico City, Mexico. November 9-11, 2022, IEEE, Centro de Investigación y de Estudios Avanzados del Instituto Politécnico Nacional Address: Av. Instituto Politécnico Nacional No. 2508 (corner with Av. Ticomán), Col. San Pedro Zacatenco, C.P. 07360 Alcaldía Gustavo A. Madero Mexico City. MEXICO , MEXICO City, , 2, 44874, 44876
3. A.Krishna Kumar, D.Deepika(2020), "Mathematical Modelling of Full Adder using Carbon Nanorods", International Journal of Innovative Technology and Exploring Engineering, Volume-9 Issue-7, pp 18-21.
4. D. Deepika, a Krishna Kumar, Monelli Ayyavaraiah, Shoban Babu Sriramoju(2019), "Phases of Developing Artificial Intelligence and Proposed Conversational Agent Architecture", International Journal of Innovative Technology and Exploring Engineering, Volume-8 Issue-12, pp 3882-3886.
5. Krishna Kumar,(2019)"Design of Voltage Regulator for High Speed Applications", International Journal of Research In Electronics and Computer Engineering, Vol. 7 Issue 1 ,pp 1625-1629
6. Krishna Kumar, G. Ashritha, D. Deepika(2018) "Design of Vending Machine Using Verilog HDL", Journal of Emerging Technologies and Innovative Research, Volume 5, Issue 7, pp 1346-1350
7. D. Deepika, A. Krishna Kumar, K.Swetha Reddy, K.Shiva Adithya (2017) "Finger Vein Authentication using Biometric Graph Method", International Journal of Research (IJR), Vol-04, Issue-14,pp 637-643.

