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Chaitanya Bharathi Institute of Technology
Gandipet, Hyderabad - 500 075 INDIA

NOTIFICATION

Ref. No: 05/Projects/HR/CBIT/Advt./2023

Date: 20th September, 2023

The Chaitanya Bharathi Institute of Technology (CBIT), Gandipet, Hyderabad – 500075 invites applications for the posts of Project Associate-II/I (PA-II/I) under the ongoing MeitY-C2S project. The appointment is initially for a period of six months and is likely to be extended for the duration of the project depending on the performance. The appointment is purely temporary and does not confer any right whatsoever to claim for absorption into the Institution. The Institute shall not have any liability in respect of the continuation of service or otherwise. An undertaking to this effect shall have to be submitted at the time of joining the duty.

Project Title: *The Design, Fabrication and development Silicon Proven IP Core for High Resolution ADPLL (5 Years)*

Post	Essential Qualifications	Desirable Qualifications	Pay-per month	Age Limit
PA-I	1. Post Graduate / Graduate degree in Electronics and Communication Engineering or equivalent with first class, preferably in VLSI or related fields	1. GATE/NET qualified. 2. Experience in Verilog HDL. 3. MATLAB experience 4. Experience in Analog IC Design and Cadence Virtuoso.	Rs.31,000 + HRA (for GATE/NET qualified) or Rs.25,000+HRA (for others)	35 years (Age Relaxation applicable as per DST norms)
PA-II	1. Post Graduate / Graduate degree in Electronics and Communication Engineering or equivalent with first class, preferably in VLSI or related fields 2. Minimum two (2) years' experience in Research and Development (VLSI)	1. GATE/NET qualified. 2. Experience in Verilog HDL. 3. Experience in Analog Design & Cadence Virtuoso. 4. Experience in Custom Layout 5. Full custom Tape-out experience.	Rs.35,000 + HRA (for GATE/NET qualified) or Rs.28,000+ HRA (for others)	35 years (Age Relaxation applicable as per DST norms)

Duly filled application in the attached format should be mailed with the subject "Application for the post of PA-I / PA-II in MeitY-C2S Project" on or before 4:00 P.M of 26th September, 2023 (Tuesday) to jahangir_eee@cbit.ac.in (CI) with copy to principal@cbit.ac.in and hr@cbit.ac.in. Eligible candidates will be called for interview and will be informed by email / phone. Self attested Xerox copies of educational qualifications and experience in support of your claim should be attached to the mail along with the application. The prescribed qualifications are minimum and the mere fact that a candidate possessing the same will not entitle him/her for being called for the interview. The Institution reserves the right to restrict the candidates to be called for interview to a reasonable number on the basis of qualifications and experience higher than the minimum prescribed. The Institute also reserves the right either to fill up or not to fill up the posts. Further details contact: Mr. Mohd Ziauddin Jahangir, Asst. Prof., ECE. (cell: 9885717845).

 Principal

