



**CHAITANYA BHARATHI
INSTITUTE OF TECHNOLOGY (A)**

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42
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IEEE Student Branch and R&D Centre, and

Department of Electronics and Communication Engineering

Code: CBIT/R&D-ECE/TP/76/May 2021

WEBINAR NOTICE

- Topic** : VLSI Design and career perspectives
- Guest Speaker** : Dr. Veeresh Babu V, Sr Manager-Analog Design at AMS Semiconductors India.
- Venue** : Zoom app, <https://zoom.us/j/5873643565?pwd=N0g2aDlw0hXVEpUS01WR1dmNmVOdz09>
- Date** : 8th May 2021.
- Time** : 18:00 pm– 19:30pm

ABSTRACT

This webinar provides an introduction on designing of analog & digital VLSI circuits. Being one of the core subjects of the circuit branches, this webinar is aimed to cover topics like Very Large-Scale Integrated design, the process followed, different software's and design tools used so as to achieve it and finally concluding with the applications and its uses. This webinar also focuses on guiding the students on how to start VLSI circuits from a career perspective and explore the various career opportunities that it will provide.

ABOUT THE SPEAKER

Dr. Veeresh Babu V. received his PhD from BITS Pilani and M.Tech from IIT Bombay, India. He worked as team lead at Qualcomm Logic India, during 2003 to 2006. During this period, he has successfully taped out many PLL IPs and a clock chip. He is currently working as manager for analog IC design team in AMS semiconductors India. His experience in AMS includes high precision analog circuits such as voltage references, high precision temperature sensors, Sigma delta ADC's, SAR ADCs and DC-DC converter focused on automotive design flow.



He acted as technical project lead for many sensor-based ICs; this role involves complete IC development including design, layout, validation, package, production test and production ramps etc. He has a published patent, nine IEEE publications and is the recipient of best paper award in VLSI design conference. He got all India 4th rank in Gate 2001. His research interests include Battery management systems, high precision band gap references, temperature sensors and analog signal processing. He is EXCON member of IEEE CAS/EDS Hyderabad Chapter and frequently involved in delivering.

Faculty Coordinator: G. Mallikharjuna Rao, Dept. of ECE.

Student Coordinators: Sneha Bhandhavi, Mutahar Mujahid, Satvika Reddy, Annanya.

All are welcome