



Career Development Centre

Date: 25.04.2024

Silicon Labs Interviews Process

Sl.No	Company	Date	Event	Time	Venue	Eligible Branches 2025 Passout
1	Silicon Labs	26.04.2024	Interviews	9.30 AM	Placement Office Interview Rooms	BE/M.Tech/ME - CSE Allied, ECE & EEE. Note: Shortlisted students only.

Note: Documents Mandatory, College ID Card, updated resumes-2 copies, Three Photographs & All Photo Copy's Mark Sheets (from Std X), Formal Dress Compulsory.

CC to Principal, CBIT for kind information,

CC to HOD's/FPC's - CSE, IT, AI&ML, AI&DS, CSE(IoT, CS & BCT), ECE & EEE

CC to Director Academics, Jd- Academics, Director IQAC, Director AEC & Library.

CC to I/C Electrical Maintenance, I/C Maintenance, Electrician & I/C Security.

Lode

Dr.NLN Reddy Advisor-CDC