

Career Development Centre

Date: 08.08.2025

MosChip (Embedded S.W. & Analog & Digital) PPT and Written Test Process

Sl.No	Company	Date	Event	Time	Venue	Eligible Branches 2026 Graduating Batch
1	MosChip (Embedded Software & Analog & Digital)	10.08.2025	PPT	8:45 AM	MosChip Office, 7th Floor, My Home Twitza TSIIC Hyderabad Knowledge City, Panmaktha, Rai Durg, Hyderabad -500032	BE - CSE, ECE & EEE. Note: shortlisted (Embedded S.W.=36 + Analog & Digital = 65 = Total= 101) students only.
2	Main exam (Embedded Software & Analog & Digital)	10.08.2025	Written Test	9:50 AM		BE - CSE, ECE & EEE.

Note: Documents Mandatory, College ID Card, Formal Dress Compulsory.

CC to Principal, CBIT for kind information,

CC to HOD's/FPC's - CSE, ECE & EEE.

CC to Director Academics, Director IQAC, Director AEC & Library.

Dr.NLN Reddy Director-CDC