

## **Career Development Centre**

Date: 06.03.2023

## **BITSILICA Personal Interview Process**

Sl.No	Company	Date	Event	Time	Venue	Eligible Branches <b>2023 Passout</b>
1	BITSILICA	08.03.2023	Personal Interview	10.00 AM Onwards	BITSILICA, Office, Hitech City Hyderabad.	ME- Embedded & VLSI & Communication Engg.
					Office location: https://goo.gl/maps/gEu9HjFFoQWAfnwd9	(Shortlisted 8 Students only)

Note: Documents Mandatory, College ID card, Formal Dress Compulsory.

CC to Principal, CBIT for kind information,

CC to HOD ECE & FPC.

CC to Director Academics, JD-Director Academics, Director IQAC, Director AEC, Library, I/c Electrical & Security.

Dr.NLN Reddy Advisor-CDC