AICTE - Sponsored Short Term Training Programme (STTP)

on

Introduction to Research Methodologies and MATLAB Programming for Optimization Techniques

25th -30th November 2019

How to Apply

The applicants should fill their registration form through Google Forms using below link

https://forms.gle/DYPotUQTKiKhDofn6

Registration:

Location:

Organizing Committee

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Dr. V Malakonda Reddy
President, CBIT

Patron
Smt. D SandhyaSree
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Chairperson
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Dr. D Krishna Reddy, HOD/ECE
Dr. P V Prasad, COE
Dr M Balasubbareddy, Member Secretary

Convener
Dr. G Suresh Babu, Prof.& HOD/EEE

Venue: EEE Dept., CBIT, Hyderabad

For further details please contact:
E-mail: sttpeee@cbit.ac.in

Mobile: 9885308964/8096909995

CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A)

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Coordinator
Dr. M Balasubbareddy, Assoc. Prof., EEE

Co-coordinator
Dr. N V PhanendraBabu, Asst. Prof., EEE

Organised by

Department of Electrical and Electronics Engineering

Kokapet (Village), Gandipet,
Hyderabad - 500075, Telangana. www.cbit.ac.in
ISO 9001:2015 Certified Institute

Chaitanya Bharathi Institute of Technology (CBIT)

CBIT, committed to Education and Innovation for 4 Decades and inspired by the range of Government Policies and Programs to fuel the Innovation drive, through focused efforts in Research, emerged as a Home for ambitious Innovators, Visionary Thinkers and extraordinary Pioneers and has been contributing its mite towards New India's Innovation Ecosystem. Five of the Engineering Departments at the Institute are recognized as Research Centers by the Osmania University. In-order to elevate the state-of-the-art of technology, a well equipped Research and Development Centre has been established.

CBIT has established a research fund to nurture and harness the talent of the students and faculty from various disciplines. The organization provides necessary facilities for promoting basic research and to catalyze the cross-fertilization of ideas with R&D agencies in other sectors for expanding and enriching the knowledge base in their respective areas. The institute provides grants-in-aid for collaborate work or individual work related to futuristic front-line research which finds application in world class systems leading to innovations.

About the Department

CBIT started Electrical & Electronics Engineering program in 1994. The intake was increased from 60 to 120 from the academic year 2013-14. The Department is certified by ISO 9001:2015.It has got accredited by AICTE four times in the years 2004, 2008, 2013 and 2017. The Department has highly qualified and experienced Teaching and Non-teaching faculty. It has started offering PG program in Power Systems & Power Electronics since 2006. This Department is having 10 well equipped labs with latest simulation & design software. The Department is keen on developing consultancy services and welcomes funding research from outside agencies. It is enriched with 15 lakhs and 17 lakhs worth projects under AICTE-MODROBS for PE & PS Labs and Electrical Machines lab respectively.

About the STTP

The growing interest in the application of Artificial Intelligence (AI) techniques to power system engineering has laid an avenue for its state-of-the-art technology. AI techniques, unlike strict mathematical methods, have the apparent ability to adapt to nonlinearities and discontinuities commonly found in power systems.

This STTP gives an overview of modern heuristic optimization techniques, fundamentals of evolutionary computation, optimization techniques, hybrid systems and their applications. The other features like heuristic approach to power system's security assessment, generation, economic load dispatch, FACTS, power system maintenance, operation and control also will be figured in the program.

It is hoped that this STTP will enable the participants to understand the concepts of optimization, to model any kind of optimization problems mathematically and to codify any type of algorithm in MATLAB environment.

Topics to be covered in STTP

- Introduction to research methodologies
- MATLAB programming, Scalar, Vector Matrices, Plots, Functions
- Optimization algorithms
- Hybridization of different algorithms
- Applications of optimization algorithms in Electrical Engineering

Resource Persons

Resource Persons will be from Renowned Institutions such as IIT's/NIT's, Industry and host Institution.

Registration Fee: Free

Accommodation

Twin/Three sharing Accommodation will be provided only to outstation participants on first come first serve basis.

Travelling Allowance (TA)

Travelling Allowance (Sleeper Class train or Bus fare by shortest route) will be reimbursed to outstation participants as per norms on submission of original tickets.

Target Group

The present STTP is expected to benefit engineers, scientists, researchers and academicians who are working/ teaching/studying in the area of optimizationTechniques.

Important Dates

Last date for Registration	:	**04Nov2019**
Intimation of selection	:	**10 Nov 2019**