

Name of Faculty Mr. M.Sri Suresh  
 Designation Asst. Professor  
 Nature of Job/Appointment Contract



Date of Joining 06-03-2024

E-mail mavurisirisuresh\_eee@cbit.ac.in

Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (Electrical Engineering)	Pursuing
PG	M. Tech. (Power Systems)	First Class
UG	B. E (EEE)	First Class

Work Experience

Teaching	07 Years 6 months
Research	---
Industry	--
Others	--

Area of Specialization

Power Systems

Professional Memberships

Member, IAENG Number: 233185.

Responsibilities held at Institution Level

--

Responsibilities held at Department Level

--

Research Guidance

--

Awards Received

--

Courses Handled at Under Graduate / Post Graduate Level.

1. Electromagnetic Field theory
2. Electrical Circuits.
3. Power System Analysis and stability.
4. Power System Operation and Control.
5. Basic Electrical Engineering.
6. Network Analysis.
7. Control Systems.
8. Power Systems –II.
9. Electrical Circuit analysis.

No. of Papers Published

National Journals – 02 International Journals – 04  
 National Conference – --- International Conference – 02

Projects Carried out

--

Patents

Technology Transfer

--

Invited Speaker

No. of Books/Chapter Published with details

Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops/Other Trainings (Attended and/or Organized).

1. I have participated in one week AICTE STTP on “Smart Microgrid and its Future Trends” Organized by Department of Electrical & Electronics Engineering, Lendi Institute of Engineering & Technology (A), Jonnada, Vizianagaram from 14/06/2021 to 19/06/2021.
2. I have participated in one week Faculty Development Program on “Contemporary Challenges in Electrical Engineering and Aiding Technologies” Organized by Department of Electrical & Electronics Engineering, St. Joseph College of Engineering from 06/07/2020 to 10/07/2020.
3. I have participated in one week Faculty Development

- Program on “Power Electronics Applications in Power Systems” Organized by Department of Electrical & Electronics Engineering, MVGR College of Engineering (Autonomous) from 17 th to 22 nd December 2018.
4. I have participated in one week AICTE STTP on “Smart Microgrid and its Future Trends” Organized by Department of Electrical & Electronics Engineering, Lendi Institute of Engineering & Technology (A), Jonnada, Vizianagaram from 14/06/2021 to 19/06/2021.
  5. I have participated in one week Faculty Development Program on “Contemporary Challenges in Electrical Engineering and Aiding Technologies” Organized by Department of Electrical & Electronics Engineering, St. Joseph College of Engineering from 06/07/2020 to 10/07/2020.
  6. I have participated in one week Faculty Development Program on “Power Electronics Applications in Power Systems” Organized by Department of Electrical & Electronics Engineering, MVGR College of Engineering (Autonomous) from 17 th to 22 nd December 2018.
  7. I have participated in A One-Day Workshop on “Transition of power sector in India to meet futuristic energy needs, challenges and issues” in GITAM on 21 st February 2019.
  8. I have participated in Python Programming Workshop conducted by ML Workshops (a subsidiary of Solivar labs) in GayatriVidya Parishad College of Engineering (Autonomous) from 21 st May to 31 st May 2018.
  9. I have participated in A One-Day workshop on “Modeling, Simulation & Analysis using Saber RD” Organized by Department of Electrical & Electronics Engineering, Gayatri Vidya Parishad College of Engineering (Autonomous) on 8 th December 2017.
  10. AICTE Sponsored Six Days Online Short Term Training Program (STTP) “TECHNICAL WRITING & RESEARCH METHODOLOGY” held on December 14 – December 19, 2020 organized by Department of Electrical and Electronics Engineering, Chaitanya Bharathi Institute of Technology, Proddatur, in association with All India Council for Technical education.
  11. A Five day National FDP on “Micro-Controller Based Embedded System Design” on on-line during 04-05-2020 to 08-05-2020.

Details of Journal Publications/  
Conferences (National and  
International)

స్వయం తేజస్విన భవ

**International Journals :**

1. “Optimal Scheduling of Distributed Generators in Multi-Microgrid Environment” in International Journal of Applied Power Engineering (IJAPE) Volume 07, Issue 06, 2023. (Scopus Indexed)
2. “Deep Neural Network based intelligent Multi-Microgrid Energy Management” in 2023 IEEE 3rd International Conference on Sustainable Energy and Future Electric Transportation (SeFeT), DOI:10.1109/SEFET57834.2023.10245648. (Scopus Indexed)
3. “Interconnected Microgrids: A Review and Future perspectives” in 2022 IEEE 3rd International Conference on Sustainable Energy and Future Electric Transportation (SeFeT), DOI:10.1109/SEFET55524.2022.9908988. (Scopus Indexed)
4. “Design and Development of Anti-lock braking system for Electric Vehicle” in International Journal of Scientific Research in Engineering and Management (IJSREM) Volume 06, Issue 05, May 2022, Impact Factor: 7.185, ISSN: 2582-3930.
5. “Enhancement of solar tracking mechanism using Arduino” in International Journal of interdisciplinary and multidisciplinary research Volume 06, Issue 07, July 2021, ISSN: 2456-4567.
6. “A Novel Control for Reduction of Voltage Sag / Swell with Reduced Rating Dynamic Voltage Restorer” in GVPR Journal of Science Technology and management Volume-1, Issue-1, 2018.
7. “Enhancement of Power Quality using Dual Unified Power Quality Conditioner by Fuzzy Logic Controller” in GVPR Journal of Science Technology and management Volume-2, Issue-1, 2018.

8. "Enhancement of Power Quality for Non-linear loads Using SHPF-TCR Device with Induction Motor" having ISBN: 978-81-930654-5-8 on 19<sup>th</sup> March 2016.
9. "Advanced Intelligent Control for Power Quality with Reduced Rating DVR" in International Journal of Electrical and Electronics Engineers (ISSN 2321-2055) on 11<sup>th</sup> August 2016.

