Name of Faculty	Mohd ZIAUDDIN JAHNAGIR	
Designation	Assistant Professor	
Nature of Job/Appointment	Regular	
Date of Joining	21 – 09 - 2014	
E-mail	Jahangir_ece@cbit.ac.in	

Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (ECE)	Pursuing
PG	M. E (Embedded Systems &VLSI)	First
UG	B. E. (ECE)	First /Distinction

Work Experience	
Teaching	5 Years
Research	--
Industry	01 years
Others	--

Area of Specialization	Analog IC Design, Digital IC Design, RF IC Design
Professional Memberships	--
Responsibilities held at Institution Level	
Responsibilities held at Department Level	Member, Course Expert Group
Research Guidance	--
Awards Received	Received the 'Best Paper' award at IEEE INDICON-2015, International Conference held at Jamia Milia University, New Delhi, December 2015.
Courses Handled at Under Graduate / Post Graduate Level.	Analog and Mixed IC Design, Analog & Digital CMOS IC Design, Analog Electronics, Control Systems, SoC Design, Computer Architecture and Organization, Computer Organization and Microprocessor, Embedded Systems, Microprocessors and Microcontrollers, RTOS, RTOS & Design Verification Lab, Embedded Systems Lab, MPMC Lab
No. of Papers Published	National Journals – Nil International Journals – 01
	National Conference – Nil International Conference – 05
Projects Carried out	--
Patents	--
Technology Transfer	--
Invited Speaker	--
No. of Books/Chapter Published with details	--
Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops.Other Trainings (Attended and/or Organized).	1. Coordinated a one week online FDP on "Research Opportunities in Image Processing and Machine Learning", conducted during June-July, 2020, Organized by the Dept. of ECE, CBIT, Hyderabad. 2. Coordinated a one week online FDP on "Outcome Based Education NBA Accreditation Process", during May-June, 2020 CBIT, Hyderabad. 3. Attended a One week Workshop on "Clock and Data Recovery Circuits" held at NIT Warangal, sponsored by SPARC, MHRD, GoI. Dated 18th – 23rd December, 2019. 4. Conducted a 12 Day SNAP CLAD training Program at CBIT, June 2019. 5. Attended a 50 Hrs (spread across 7 Week) CLAD training program for ServiceNow Application Developer at ServiceNow's Office, Hyderabad, sponsored by TASK, Govt. of Telangana, March-April 2019. 6. Recognized reviewer for Elsevier's "Microelectronics Journal".

International/National Journals from the Year 2017

M.Z. Jahangir, J. Mounika, "Design and simulation of an innovative CMOS ternary 3 to 1 multiplexer

and the design of ternary half adder using ternary 3 to 1 multiplexer", Microelectronics Journal, Volume 90, Pg.82-87, 2019 doi: https://doi.org/10.1016/j.mejo.2019.05.007.

International/National Conference Proceedings from the Year 2017

Syed Ali Imran Quadri, Mohd Ziauddin Jahangir "Design, Implementation and Performance Comparison of Different Branch Predictors on Pipelined-CPU", 2017 International Conference on Computer, Electrical & Communication Engineering (ICCECE), Kolkatta, 2017, D.O.I: 10.1109/ICCECE.2017.8526196.