Name of Faculty EDIGA CHANDRASEKHAR

Assistant Professor Designation

Nature of Job/Appointment Regular

Date of Joining 18-07 - 2012

E-mail echandrasekhar_ece@cbit.ac.in

Education Qualifications Name of the Degree Class

> Ph. D Doctor of Philosophy (ECE) Pursing

> PG M.E. (ES&VLSID) First

> UG B. Tech (Electronics Communication Engineering) First

Work Experience

Organized).

10 Years (Including Research Experience) Teaching

Research 1 Year 6 months

Industry Others

Area of Specialization Analog and Mixed Signal VLSI Design

Professional Memberships

Responsibilities held at Institution

Level

Responsibilities held at In charge of Basic Electronics lab. Department Level Member, Course Expert Group.

Research Guidance

Awards Received

Electronic Devices, Analog Circuits, IC Applications, Electronic Engineering-I, Electronic Engineering-II, Electronic Engineering, Basic Courses Handled at Under Electronics, Elements of ECE, VLSI Technology, CMOS VLSI Design, Graduate / Post Graduate Level.

VLSI Technology and Physical Design Automation.

National Journals -International Journals -No. of Papers Published

National Conference - Nil International Conference -

Projects Carried out

Patents

Technology Transfer

Invited Speaker

No. of Books/Chapter Published with details

Participated in Three Day FDP on "Recent Trends in IC Design" Details of Short-Term Training Organized by Dept of ECE, VCE Hyderabad in Association with Programs/Faculty Development CoreEL Technologies during 29-06-2020 to 01-07-2020. Programs/Seminars/Workshops. Other Trainings (Attended and/or

Successfully Completed Al for Everyone an online non-credit course authorized by deeplearning.ai and offered by Stanford University through Coursera on 25/06/2020.

- Successfully Completed COVID-19: What You Need to Know (CME Eligible) an online non-credit course authorized by Osmosis and offered through Coursera on 25/06/2020.
- Participated in webinar on "Publishing Research Articles in Scopus Indexed Journals" conducted by CBIT - School of Management Studies on 24thJune, 2020.
- Participated in One Week Online Faculty Development Program on "Outcome based Education and NBA Accreditation Process (UG) during 28-05-2020 to 01-06-2020, organized by Chaitanya Bharathi Institute of Technology, Hyderabad.
 Participated in the IIC Online Sessions conducted by Institution's
- Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 nationwide lockdown.
- Successfully Completed "TCP/IP and Advanced Topics" a 5-Week





02

- online non-credit course authorized by University of Colorado and offered through Coursera on 18-05-2020.
- Successfully Completed "Peer-to-Peer Protocols and Local Area Networks" a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 18-05-2020.
- Successfully Completed "Introduction to Electronics" a 7-Week online non-credit course authorized by Georgia Institute of Technology and offered through Coursera on 17-05-2020.
- Successfully Completed Wireless Communications for Everybody an online non-credit course authorized by Yonsei University and offered through Coursera on 17/05/2020.
- Successfully Completed "Packet Switching Networks and Algorithms" a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 17-05-2020.
- Successfully Completed "Fundamentals of Network Communications" a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 17-05-2020.
- 13. Successfully Completed "Introduction to Electronics" a 7-Week online non-credit course authorized by Georgia Institute of Technology and offered through Coursera on 17-05-2020.
- Attended a virtual classroom training on "ASIC Physical Design and Verification" conducted by Excel VLSI Technologies, Bangalore during 27-04-2020 to 02-05-2020.
- 15. Attended a Five Days Online Faculty Development Programme on "Recent Communication Tools And Its Application" Organized by Department of Electronics and Communication Engineering from 19th to 23rd May, 2020 St. Martin's Engineering College Dhulapally, Secunderabad
- 16. Successfully completed the 'Effective and Efficient Online Teaching in the Age of Corona, A Hands on Workshop 'on 17 May 2020 and attended the interaction session and successfully completed both pre and post workshop online activities, involving 3 days of video lectures and hands-on work. This training was organized by the Teaching Learning Centre (ICT) at IIT Bombay, funded by the Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMMNMTT), MHRD, Govt. of India.
- 17. Attended all Leadership Talks held by MHRD's Innovation Cell.
- Successfully completed "NAAC Awareness Programme for Faculty" organized by Marathwada Mitra Mandal Institute of Technology (MMIT), Lohgaon, Pune - 47 conducted online during the period 08/05/2020 to 14/05/2020.
- Successfully completed a 12 Week NPTEL course "Integrated Circuits MOSFETs OP-Amps and their Applications", on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 semester.
- Successfully completed a 12 Week NPTEL course "NBA Accreditation and Teaching - Learning in Engineering (NATE)", on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 semester.
- 21. Attended One-week Faculty Development Programme on "IoT for Emerging Applications" (04-08 Feb, 2020). An Initiative of Ministry of Electronics & Information Technology (MeitY), Government of India Electronics & ICT Academy IIT Guwahati, Assam, Organized in association with Chaitanya Bharathi Institute of Technology(A) Support from Finland Labs.
- 22. Successfully Completed Credit Course on "Research Methodologies" organized JNTU Kakinada 1st to 17th May,2019.
- 23. Attended MHRD, Government of India Sponsored a one Week Program on "Development of Laboratory Instruction and Manual" organized by National Institute of Technical Teachers and Research, Kolkata 4th to 9th February 2019.
- Attended one day workshop "Significance of Sanskrit for Technological Research" organized by Geervana Bhararhi of CBIT on 8th Sep 2018.

International Journal from the year 2017

- N. Alivelu Manga, E.Chandrasekhar, P. Chandra Sekhar (2019), "Design and Simulation of 12-bit Current Steering DAC", International Research Journal of Engineering and Technology (IRJET), VOLUME: 06 ISSUE: 07,-ISSN: 2395-0072
- PranayKumar Dasari1, DSaiHarsha, E.Chandrasekhar(2019) "Implementation of Low Cost Home Monitoring, Controlling and Security System using IoT", International Journal of Engineering Research & Technology (IJERT), VOLUME:8 Issue 06, ISSN: 2278-0181
- 3. Bhukya Shankar, E Chandra Sekhar (2018)" Delay, Power performance of 8-Bit ALU Using Carry

- Look-Ahead Adder with High Vt Cell", International Journal for Research in Applied Science & Engineering Technology (IJRASET), VOLUME:6 Issue 05, ISSN: 2321-9653

 4. Syed Ali Imran Quadri, J Manikya Teja, E. Chandrasekhar(2017) "Traffic Monitoring System", International Journal of Engineering Trends and Technology (IJETT), VOLUME:48 Issue 05, ISSN: 2231-5381