

Name of Faculty D. SONY
 Designation Assistant Professor
 Nature of Job/Appointment Contract
 Date of Joining 3rd July 2019
 E-mail sonyd_ece@cbit.ac.in



| Education Qualifications | Name of the Degree | Class |
|--------------------------|-----------------------------|------------------------------|
| Ph. D | | Pursuing |
| PG | M.Tech (VLSI System Design) | First Class with Distinction |
| UG | B. Tech (ECE) | First Class with Distinction |

Work Experience

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| Teaching | 10 Years |
| Research | - |
| Industry | - |
| Others | - |

Area of Specialization Digital Design, Embedded Systems

Professional Memberships

Responsibilities held at Institution Level -

- Responsibilities held at Department Level
1. CACG Coordinator (ECE) since AY16-17 to June 2019, at GCET.
 2. Techno Digest Department News Letter Editor since AY16-17 to June 2019, at GCET.
 3. NAAC Co-coordinator (ECE), since AY16-17 to June 2019, at GCET.

Research Guidance

Awards Received

Courses Handled at Under Graduate / Post Graduate Level. Digital Design, Microprocessors & Microcontrollers, Embedded System Design using RTOS, Linear Integrated Circuit and Applications, Signals and Systems. Electronic Devices and Circuits, Microprocessors & Microcontrollers Lab, Linear Integrated Circuit and Applications Lab, Electronic Devices and Circuits Lab.

No. of Papers Published
 National Journals – 0 International Journals – 05
 National Conference – 01 International Conference – 01

Projects Carried out -

Patents -

Technology Transfer -

Invited Speaker -

No. of Books/Chapter Published with details -

- Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (Attended and/or Organized).
1. 3-Day Webinar on Signal Processing Techniques for Radar Applications, organized by "Institute Of Aeronautical Engineering", on 10th, 12th and 14th July 2020.
 2. One week National level online FDP on "Cyber Security", organized by CBIT & MGIT in association with Supraja Technologies during 7th-12th July, 2020.
 3. One week online FDP on "Future Nano Electronic Devices and Circuits", organized by MGIT during 6th-10th July, 2020.
 4. One week FDP on "Outcome Based Education and NBA Accrediation Process during 28th May- 1st Jun 2020.
 5. Five Day STTP on "MATLAB based Teaching-Learning in Mathematics, Science & Engineering" organized by Ramrao Adik Institute of Technology, Nerul, Navi Mumbai in collaboration with DesignTech Systems Pvt. Ltd., Mumbai during 18th to 22nd May 2020.
 6. A Two Day National Webinar on "Artificial Intelligence and Machine Learning" organized by Loyola Academy, Secunderabad during 20th-21st, May 2020.
 7. A One Week Faculty Development Program on "Recent Trends in

Advanced Communications" organized by Department of Electronics and Communication Engineering, Lakireddy Bali Reddy College of Engineering, Mylavaram, during 4th-9th, May 2020.

8. Completed NPTEL online Certification Course on " NBA Accrediation and Teaching –Learning in Engineering" during Jan-Apr 2020.
9. Co-coordinator for A One week FDP on " Research Opportunities in Image Processing and Machine Learning", conducted by the Department of Electronics and Communication Engineering, Chaitanya Bharathi Technology, Institute of during 29th June to 03rd July 2020.

International Journal

1. D.Sony, (2018), "Design of High speed fixed and reconfigurable FIR filter for speech signal processing", *International Journal of Advance Engineering and Research Development*, Volume 05 issue 01, pp 136-148.
2. D.Sony, (2018), "Real-time Scene Text Detection via Connected Component Clustering and Non-text Filtering in videos", *International Journal of Advance Engineering and Research Development*, Volume 05 issue 02, pp 801-806.
3. D.Sony, (2018), "Comparison of Wallace, Vedic and Dadda Multipliers", *International Journal of Advance Engineering and Research Development*, Volume 05 issue 03, pp 142-151.
4. D.Sony, B.Ramu, (2018), "An adaptive image resolution enhancement by using multi resolution transform", *International Journal Of Creative Research Thoughts*, Volume 06 issue 01, pp 1235-1251.
5. D.Sony, M.Krishna, (2018), "Motion Deblurr", *International Journal of Advance Engineering and Research Development*, Volume 05 issue 03, pp 801-806.

International Conferences:

1. D. Sony, D. Krishna Reddy, P. Naveen and V. Satya Srinivas (2019) " IRNSS and GPS Satellite User Range Accuracy Analysis for Receiver Autonomous Integrity Monitoring ", National Space Science Symposium (2019).
2. D. Sony, V. Satya Srinivas, P. Naveen and D. Krishna Reddy, (2019), "IRNSS User Range Accuracy Evaluation for Receiver Autonomous Integrity", URSI AP-RASC (2019).