



CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A)
ACADEMIC AND EXAMINATION CELL

No. CBIT/AEC/ Pract-R13 /2019-20/1

Date: 14.11.2019

Scheme of Practical Examination for **B.E. 4/4 - I Semester (Supple) Dec-2019.**

COMPUTER SCIENCE ENGINEERING

Batch No.		Hall Ticket Nos.	Strength	Date	Object Oriented System Development Lab (CS 416)
1.	CS	1601-14-733-161	01	06.12.2019	10 to 1.00 pm

ELECTRONICS AND COMMUNICATION ENGINEERING

Batch No.		Hall Ticket Nos.	Strength
1.	ECE	1601-15-735-116 (EDA & AS)	01

Date	Electronic Design & Automation Lab (EC 415)	Date	Advanced Simulation Lab (EC 416)
04.12.2019	10 to 1.00 pm	05.12.2019	10 to 1.00 pm

Note : Any Other Supplementary Registered Candidates may be allowed

To
The Notice Board (AEC, CSE & ECE)
Copy to

1. The Head, Dept. of CSE & ECE
2. The Internal Examiners concerned.
3. The Controller of Examinations, CBIT


PRINCIPAL