Name of Faculty	B. NEERAJA
Designation	Assistant Professor
Nature of Job/Appointment	Regular
Date of Joining	28-01-2011
E-mail	bneeraja_ece@cbit.ac.in

Education Qualifications	Name of the Degree	Class
Ph.D	Ph.D.	Registered
PG	M.E.(Digital Systems)	Distinction
UG	B.TECH(ECE)	Distinction

Work Experience	
Teaching	9 Years
Research	3years
Industry	--
Others	06 Months

Area of Specialization	Signal Processing,Wireless Communication
Professional Memberships	--
Responsibilities held at Institution Level	1. Member of AICTE Disciplinary Committee-2019. 2. Coordinator of student clubs Chaitanya Seva-(Street cause& Engineers without borders), Chaitanya Kreeda-spoorts, Chaitanya Sudhee-2019.
Responsibilities held at Department Level	1. Member, Course Expert Group-2019. 2. Member, Common Course Commitee -2019. 3. In-charge, National Career Services -2019. 4. In-charge, EC Lab-1 from 2013.
Research Guidance	--
Awards Received	--
Courses Handled at Under Graduate / Post Graduate Level.	Electronic Devices and Circuits,Analog Circuits,Basic Electronics,Elements of Electronics and Communication Engineering,Wireless Mobile Communication,Radar and Satellite Communication,Coding Theory and Techniques
No. of Papers Published	National Journals – 00 International Journals – 01 National Conference – 01 International Conference – 04
Projects Carried out	--
Patents	--
Technology Transfer	--
Invited Speaker	1. Resource person for the five day intensive course on "Hands on LabVIEW" under LabVIEW academy school from 21/04/18 to 28/04/18. 2. Delivered a lecture in a four day intensive course on "Hands on Lab VIEW" under LabVIEW academy school from 27/11/17 to 30/11/17.
No. of Books/Chapter Published with details	--
Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops.Other Trainings (Attended and/or Organized).	1. Participated in the IIC Online Sessions conducted by Institution's Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 nationwide lockdown. 2. Successfully Completed "MATLAB Onramp" online course authorized by Mathworks Training Services on 19/05/2020. 3. Successfully Completed "Introduction to Electronics"an online non-credit course authorized by Georgia Institute of Technology and offered through Coursera on 14/05/2020. 4. Successfully Completed "AI for Everyone"an online non-credit course authorized by Stanford University and offered through Coursera on 02/05/2020. 5. Successfully Completed "LabVIEW Core 2" online course authorized by NI Customer Education on 27/04/2020.

6. Successfully Completed "LabVIEW Core 1" online course authorized by NI Customer Education on 21/04/2020.
7. Successfully Completed "Wireless Communications for Everybody"an online non-credit course authorized by University of Yonsie and offered through Coursera on 06/04/2020.
8. A One Week online Faculty Development Program on "LaTeX & Technical Report", 25 - 30 May, 2020 organized by Department of Mathematics,Kakatiya University, Warangal, Telangana, in association with Spoken Tutorial, IIT Bombay.
9. Successfully Completed a One Week Faculty Development Program on "Outcome based education and NBA accredation process"organised by CBIT, Hyderabad, during 28/05/20 to 01/06/20.
10. A One Week online Faculty Development Program on "Recent trends in Advanced Communication", Lakireddy Bali Reddy College Of Engineering, Mylavaram, Krishna(Dt) from 04th -9th may 2020.
11. Successfully Completed NPTEL-AICTE course on"Principles and Techniques of Modern Radar Systems", during Jul-oct, 2019.
12. Participated in one day workshop on "Significance of MATLAB in Applications of Emerging Technologies" Department of Electrical and Electronics Engineering, CBIT, Hyderabad on 26th June 2019.
13. A One Week FDP on "5G Wireless Communication" organized by Electronics and ICT Academy, NIT Warangal. Meity, Govt. of INDIA, during29th May to 3rd June,2019.
14. Attended 1 week short term training programme through ICT mode on "Development of Laboratory Instruction and Manual "organized by CBIT,Hyderabad during 4 feb to 9 feb 2019.
15. Qualified and Certified LabVIEW Associate developer (CLAD) exam and recognized as one of the member in "NI LabVIEW" academy from Aug 2017 to Aug 2019.
16. Organized a Two week workshop on "Hands on Lab VIEW" under Lab VIEW academy school during 18/06/18 to 30/06/18.
17. Attended a two week refresher course on "Spectral Methods for Engineering & Scientific computing Applications" at IIT Bhubaneswar, Govt.of India, MHRD on 5 th – 16 th June 2017.
18. Attended a workshop on Matlab Expo 2017 and gained knowledge in HDL code writing.
19. Attended a one-week workshop on "Hands on myDAQ and myRIO" during "6th – 11th Nov'17(1 Week) Organized by Dept. of ECE (Under LabVIEW Academy school).
20. Attended a four day Faculty development course on "Certified LabVIEW Associate Developer" during 21st – 24th Aug' 17 (4 days) organized by Dept. of ECE,CBIT,Hyderabad.

International/National Journals from the Year 2017

Neeraja B, Ramya K "Design and Analysis of an Energy Efficient 4-bit Barrel Shifter Circuits in 45nm Technology", International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 – 8958, Volume-9 Issue-4, April, 2020.

International/National Conference Proceedings from the Year 2017

1. B.Neeraja, N .V. Koteswara Rao, B. Rajendra Naik," Detection and Analysis of closely spaced Multiple Targets using Modified MUSIC method",Scopus indexed Second International Conference on Computing, Communication and Energy Systems 2020 (ICCCES 2020), MEA College of Engineering, Perinthalmanna, Kerala, India, 2020, pp. 158-163.
2. Neeraja Bandi, Sathish kumar meda," Multiply-Accumulate unit for Binary Arithmetic using high speed adders and PIPO Accumulator", Scopus indexed Second International Conference on Computing, Communication and Energy Systems 2020 (ICCCES 2020), MEA College of Engineering, Perinthalmanna, Kerala, India, 2020, pp. 1-5.
3. B. Neeraja and R. S. P. Goud, "Design of an Area Efficient Braun Multiplier using High Speed Parallel Prefix Adder in Cadence," 2019 IEEE International Conference on Electrical, Computer and Communication Technologies (ICECCT), Coimbatore, India, 2019, pp. 1-5, doi: 10.1109/ ICECCT.2019.8869307.