

Name of Faculty A. KRISHNA KUMAR  
 Designation Assistant Professor  
 Nature of Job/Appointment Regular  
 Date of Joining 26-12-2005  
 E-mail krishnakumar\_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
PG	M. Tech (VLSI Design)	First
UG	B. E.(ECE)	First

Work Experience

Teaching	15 Years
Research	--
Industry	01
Others	--

Area of Specialization Digital Design, VLSI Design, FPGA Architectures

Professional Memberships --

Responsibilities held at Institution Level In-Charge, Intercom

- Responsibilities held at Department Level
1. In-charge for ISO
  2. Member, Course Expert Group
  3. In-charge-Campus Radio
  4. In-charge-SSP/HDL Lab

Research Guidance --

Awards Received --

Courses Handled at Under Graduate / Post Graduate Level. VLSI Design, Verilog HDL, CPLD & FPGA Architectures, Electronic Devices and Circuits, Basic Electronics, Digital Integrated Circuits and Applications, Advanced VLSI Design, VLSI Technology, Pulse and Digital Circuits, Advanced Digital Design through VHDL.

No. of Papers Published

National Journals – 0	International Journals – 06
National Conference – 03	International Conference – 00

Projects Carried out --

Patents --

Technology Transfer --

Invited Speaker --

No. of Books/Chapter Published with details --

- Details of Short-Term Training Programs/Faculty Development Programs/Seminars/ Workshops/ Other Trainings (Attended and/or Organized).
1. Participated a One-week Online FDP on “Research Opportunities in Image Processing and Machine Learning”, conducted by the Department of ECE, CBIT, during 29<sup>th</sup> to 03<sup>rd</sup> July 2020.
  2. Passed the course “NBA Accreditation and Teaching – Learning in Engineering (NATE) – NPTEL Online Certificate.
  3. Successfully attended Leadership Talk with Shri Dipendra Manocha, (Motivational Speaker), by MHRD’s Innovation Cell, held on 27<sup>th</sup> June 2020.
  4. Attended One-week online FDP on “Recent Trends in VLSI Design” organized by the Department of ECE, Lakshmi Narain College of Technology & Science, Bhopal MP, from 22<sup>nd</sup> to 26<sup>th</sup> June 2020.
  5. Participated in webinar on “Publishing Research Articles in Scopus Indexed Journals” conducted by CBIT – School of Management Studies on 24<sup>th</sup> June 2020
  6. Attended Leadership Talk with Mr. Shridhar Venkat, CEO (Akshay Patra Foundation) by MHRD’s Innovation Cell, held on 20<sup>th</sup> June.
  7. Participated in online STTP on “An Emerging Paradigm of Low Power Computational VLSI Design” BVRIT, from 16<sup>th</sup> to 20<sup>th</sup> June 2020.
  8. Successfully completed One-Week online FDP on “Recent

trends in VLSI”, conducted by Department of ECE, GRIET during 2<sup>nd</sup> to 7<sup>th</sup> June 2020.

9. Successfully participated in Webinar organized by Internal Quality Assurance Cell (IQAC), Rai University, Ahmedabad, Gujarat, on “How to publish Research Article in SCI Journals using Info-Graphic Tools” held on 4<sup>th</sup> June 2020.
10. Attended One-week online FDP on “opportunities & Challenges in Electronics & Allied Industries in India post COVID-19” organized by Department of Electronics Engineering in association with Institution’s Innovation Council (IIC) of Vivekanand Education Society’s Institute of Technology from 25<sup>th</sup> to 30<sup>th</sup> May 2020.
11. Successfully completed “VLSI CAD Part I : Logic ” an online non-credit course authorized by Urbana-Champaign and offered through Coursera, May 2020.
12. Successfully completed “AI for Everyone” an online non-credit course authorized by deeplearning.ai and offered through Coursera, May 2020.
13. Participated and Completed 5 Days Online FDP on “R Programming” organized by Indo Global Group of Colleges, New Chandigarh in association with IIT Bombay Spoken Tutorial ( An Initiative of National Mission on Education through ICT, MHRD, Govt. of India) from 27<sup>th</sup> April to 1<sup>st</sup> May 2020.
14. Successfully completed One-week National Faculty Development Program and Online Training on “LaTeX” Organised by Sanjay Ghodawat University, Kolhapur in association with Spoken Tutorial Project, IIT-Bombay during 27<sup>th</sup> April to 2<sup>nd</sup> May 2020.
15. Attended a Three-day online workshop on “Education 4.0” organized by IQAC, Atharva College of Engineering held on 28<sup>th</sup>-30<sup>th</sup> April 2020.
16. Participated and Successfully completed AICTE Training and Learning (ATAL) Academy Online FDP on “Internet of Things (IoT)” at National Institute of Technical Teachers Training and Research, Chandigarh from 20<sup>th</sup> to 24<sup>th</sup> April 2020.
17. Successfully completed “VLSI Digital Design using Verilog and hardware: Handson\_temp” online course on 15<sup>th</sup> April 2020.
18. Participated in Mentor Webinar on “Analog IC Design using Mentor EDA Tools” in association with CoreEL Technologies and Mentor held on 10<sup>th</sup> April 2020.
19. Successfully completed online course “Foundations to Open Education and OERs repositories” MOOC Course UNED Abierta (20-04-2020 to 30-06-2020) on 6<sup>th</sup> April 2020.
20. Successfully completed one-week FDP on “IoT for emerging Applications” organized by E&ICT academy, IIT Guwahati in association with Department of ECE, CBIT, held from 04-08 February 2020.
21. Participated in one day Symposium on Significance of Sanskrit for Technological Research, in association with Samskritha Bharathi, Telangana organized by Geervana Bharathi of CBIT on 8<sup>th</sup> September 2018.

#### **International/National Journals from the Year 2017**

1. Krishna Kumar, G. Ashritha(2020) “Chat Application via UDP Protocol in C” , International Journal for Research in Applied Science & Engineering Technology (IJRASET), Volume 8 Issue V,pp 1219-1324.
2. A.Krishna Kumar, D.Deepika(2020), “Mathematical Modelling of Full Adder using Carbon Nanorods”, International Journal of Innovative Technology and Exploring Engineering, Volume-9 Issue-7, pp 18-21.
3. D. Deepika, a Krishna Kumar, Monelli Ayyavaraiah, Shoban Babu Sriramoju(2019), “Phases of Developing Artificial Intelligence and Proposed Conversational Agent Architecture”, International Journal of Innovative Technology and Exploring Engineering, Volume-8 Issue-12, pp 3882-3886.
4. Krishna Kumar,(2019)“Design of Voltage Regulator for High Speed Applications”, International Journal of Research In Electronics and Computer Engineering, Vol. 7 Issue 1 ,pp 1625-1629
5. Krishna Kumar, G. Ashritha, D. Deepika(2018) “Design of Vending Machine Using Verilog HDL”, Journal of Emerging Technologies and Innovative Research, Volume 5, Issue 7, pp 1346-1350
6. D. Deepika, A. Krishna Kumar, K.Swetha Reddy, K.Shiva Adithya (2017) “Finger Vein Authentication using Biometric Graph Method”, International Journal of Research (IJR), Vol-04, Issue-14,pp 637-643.