

## Chaitanya Bharathi Institute of Technology (A)

The Department of Electronics and Communication Engineering

### Workshops

The Department of Electronics and Communication Engineering at Chaitanya Bharathi Institute of Technology (CBIT) successfully organized a two-day workshop on "Building Blocks of VLSI Design" on March 20-21, 2025. The event was conducted under the Chip to Startup (C2S) Programme of the Ministry of Electronics and Information Technology (MeitY), Govt. of India, in collaboration with the IEEE - CAS Society, Student Chapter, CBIT.



Speaker: Dr. Krishnakanth (CTO, MOSat Labs)



Speaker: Dr. Abhishek Srivastav, IIT, Hyderabad



Participants of the workshop

- The Department of Electronics and Communication Engineering at Chaitanya Bharathi Institute of Technology (CBIT) and IETE successfully organized a one day VLSI WORKSHOP on "Circuit Simulation using PSpice" on 25-03-2025 by Dr. Vasanth Kishore, Assoc.Professor, HoD, ECE Dept.
- <https://drive.google.com/file/d/1VnS11JEynVPKVW0JJJa8E6-ITy3zWGw0/view?usp=sharing>